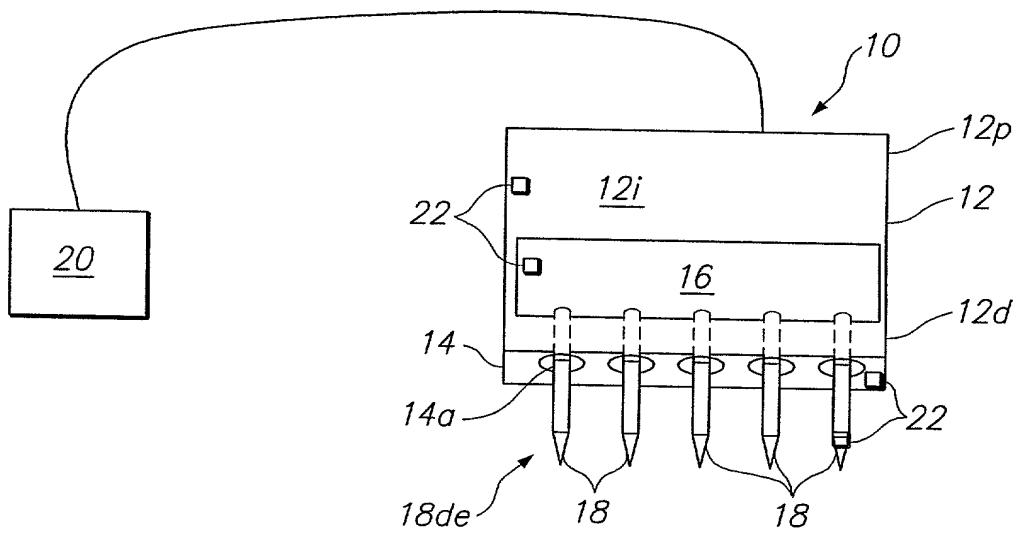
**FIG. 1****FIG. 2**

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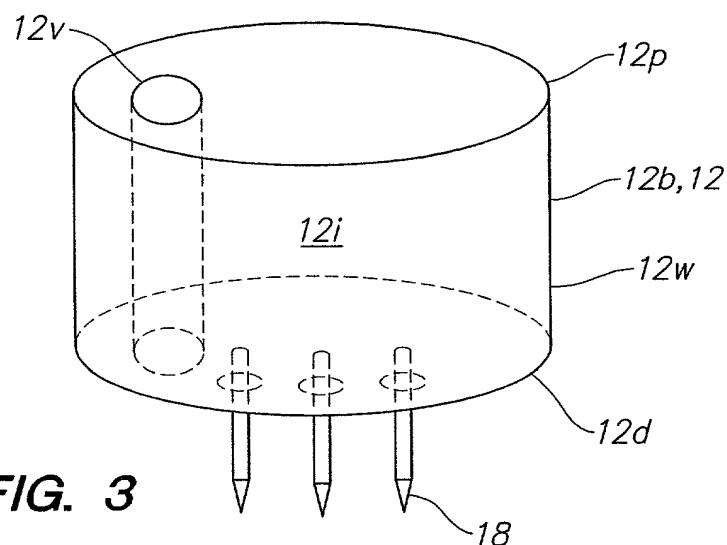


FIG. 3

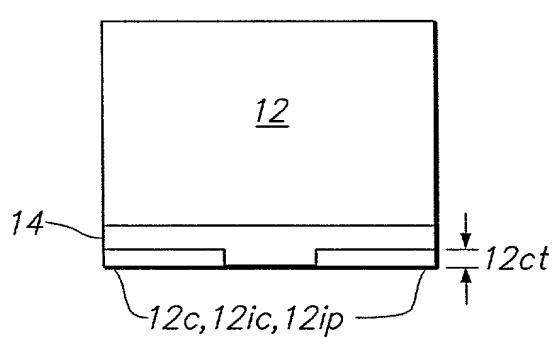


FIG. 4A

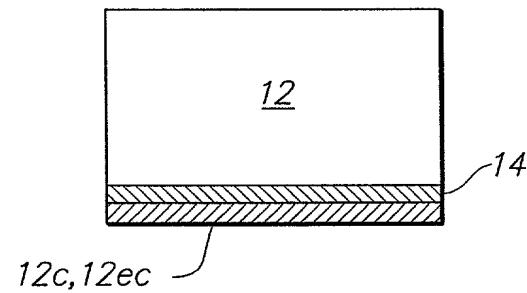


FIG. 4B

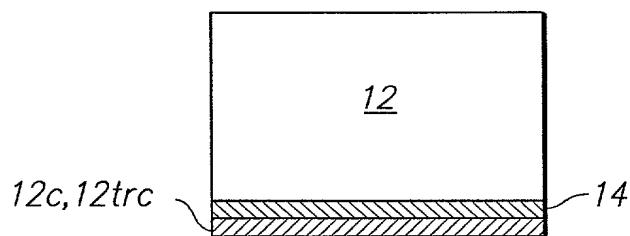


FIG. 4D

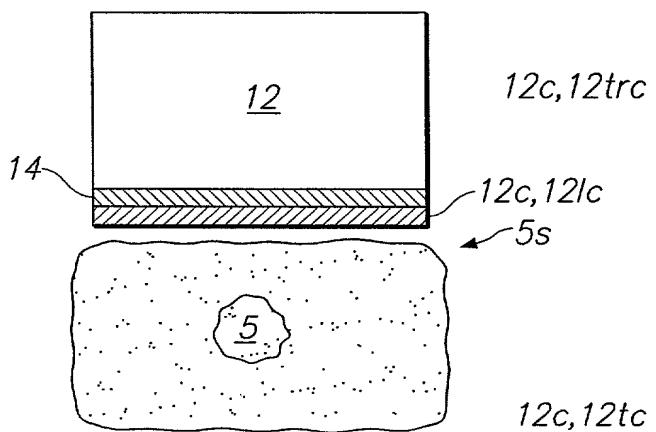


FIG. 4C

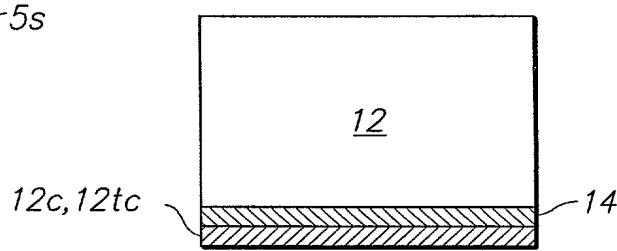


FIG. 4E

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FIG. 5C

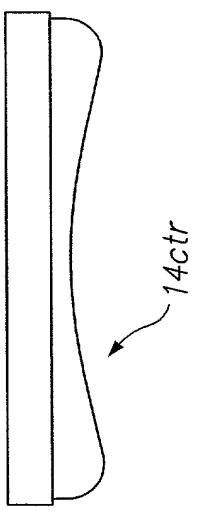


FIG. 5B

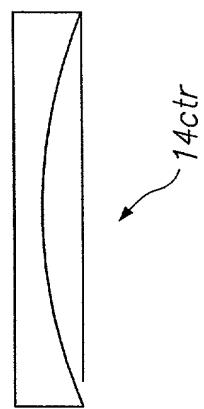
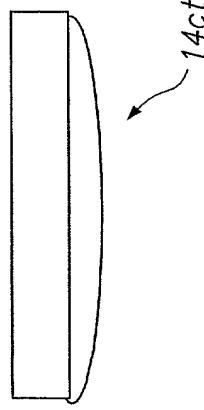
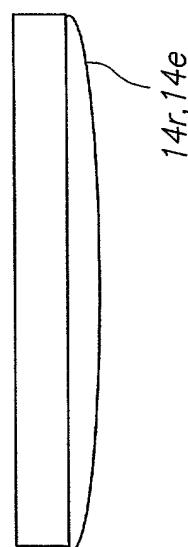


FIG. 5A



14ctr



14r, 14e

FIG. 5E

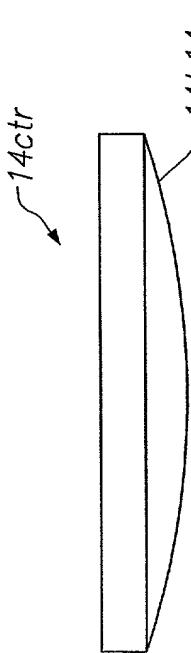
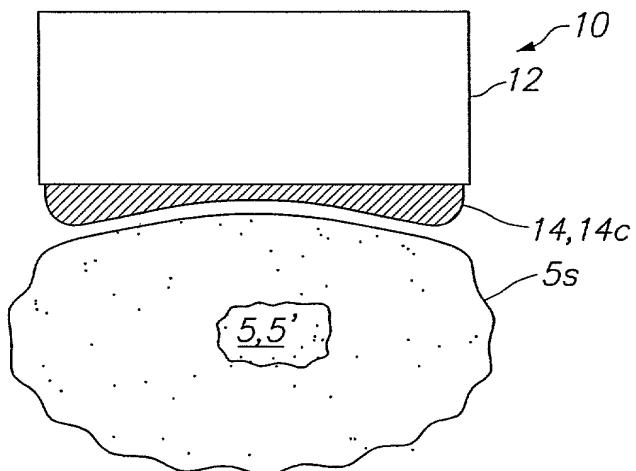
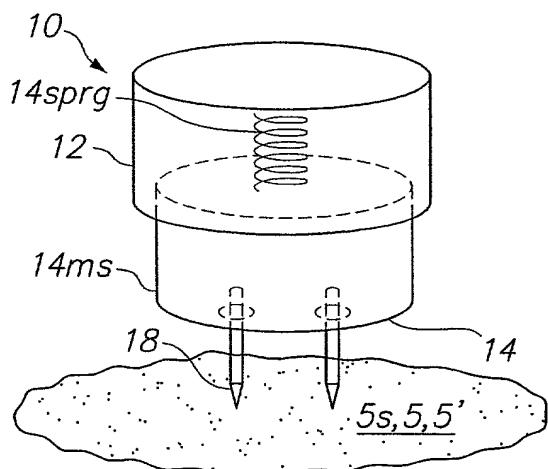
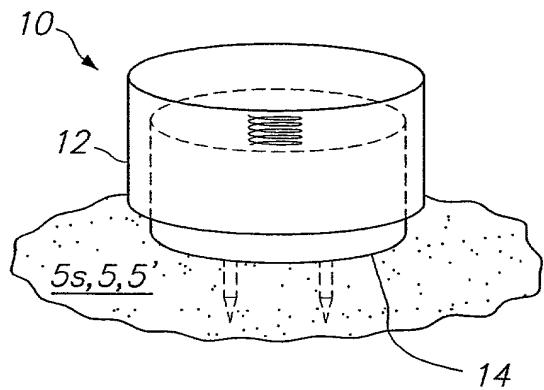


FIG. 5D

**FIG. 6****FIG. 7A****FIG. 7B**

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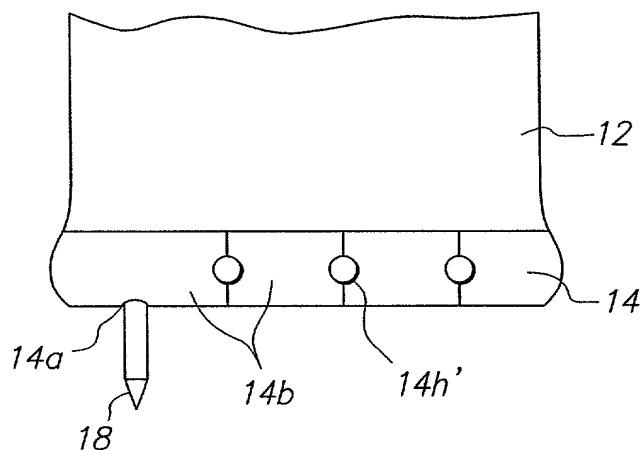


FIG. 8

FIG. 9A

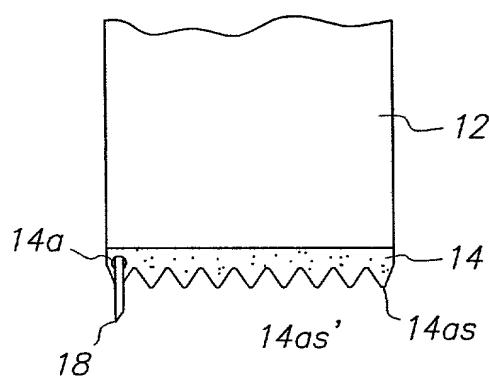


FIG. 9B

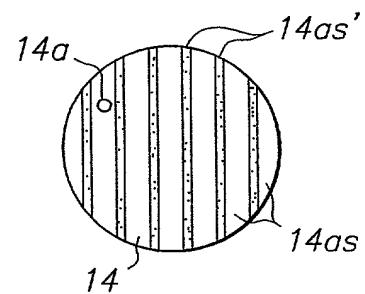


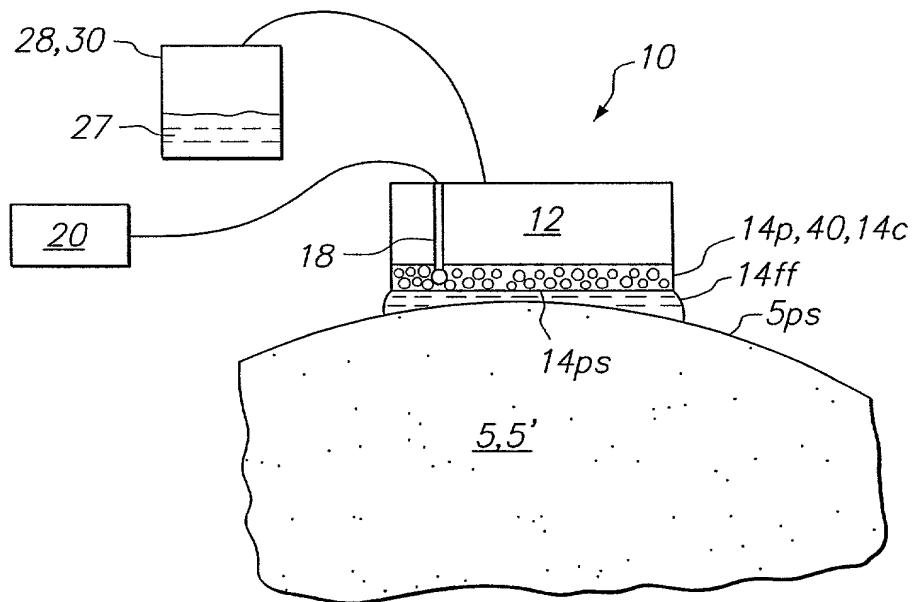
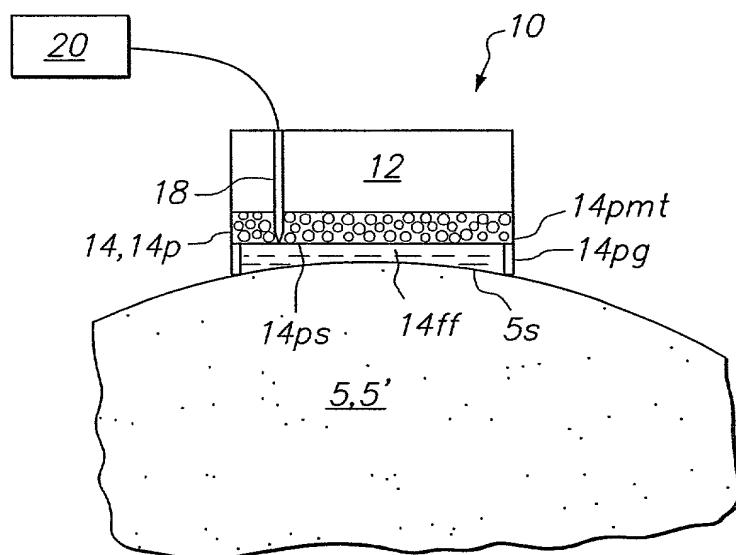
FIG. 10**FIG. 11**

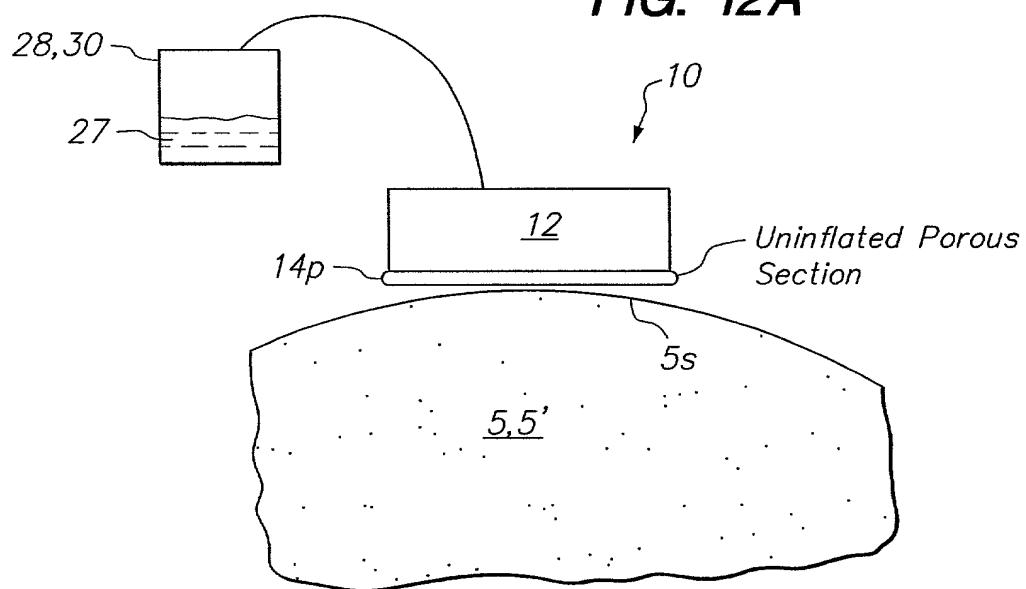
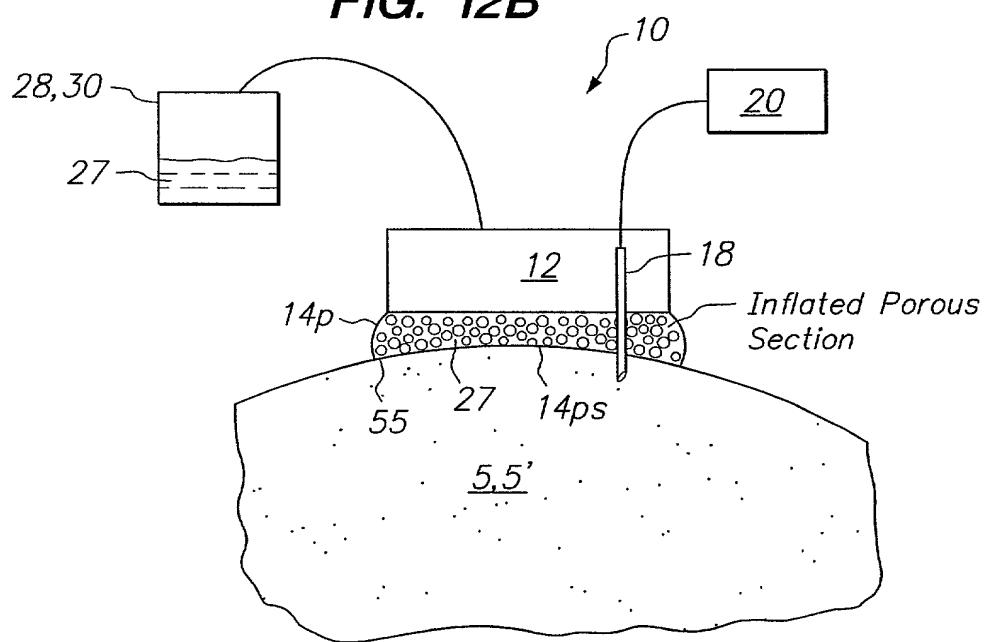
FIG. 12A**FIG. 12B**

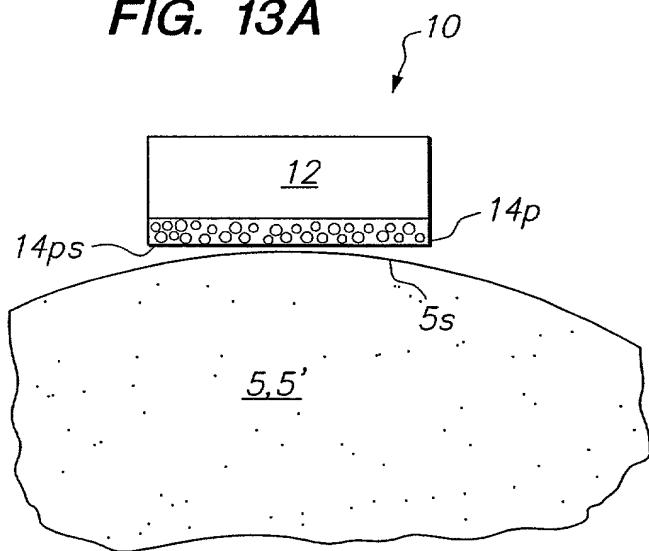
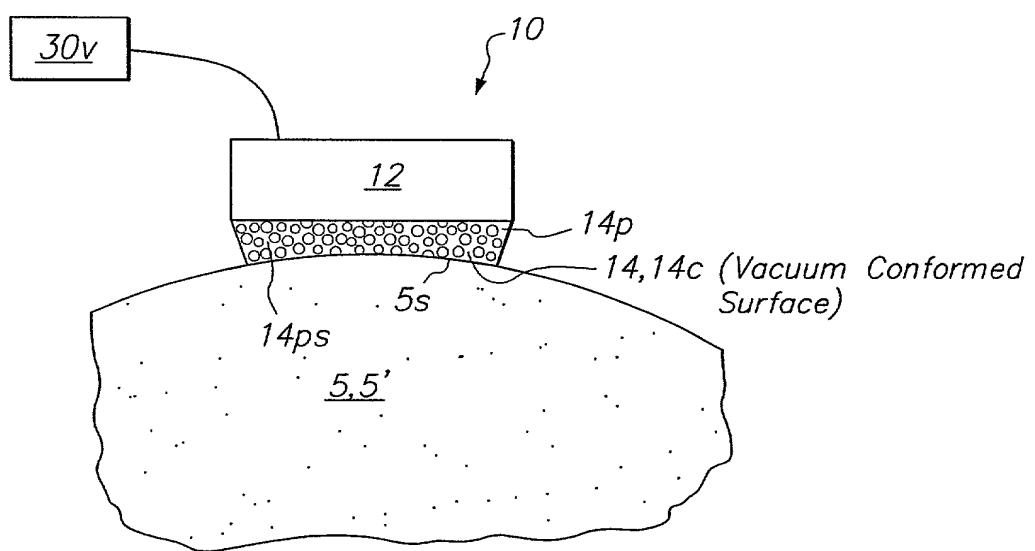
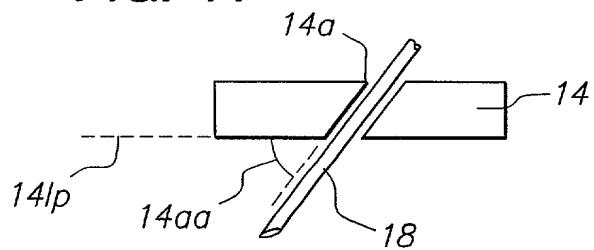
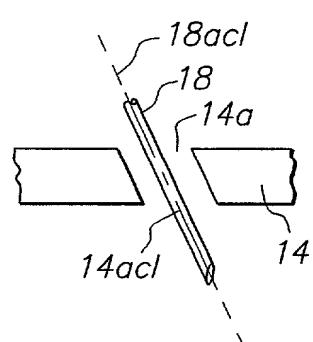
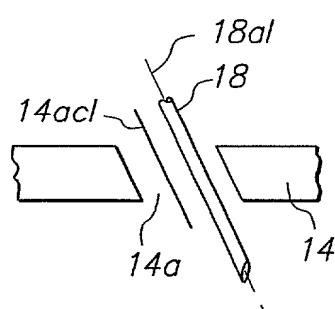
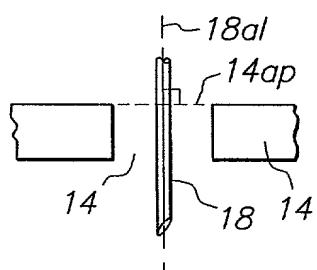
FIG. 13A**FIG. 13B**

FIG. 14**FIG. 15A****FIG. 15B****FIG. 15C**

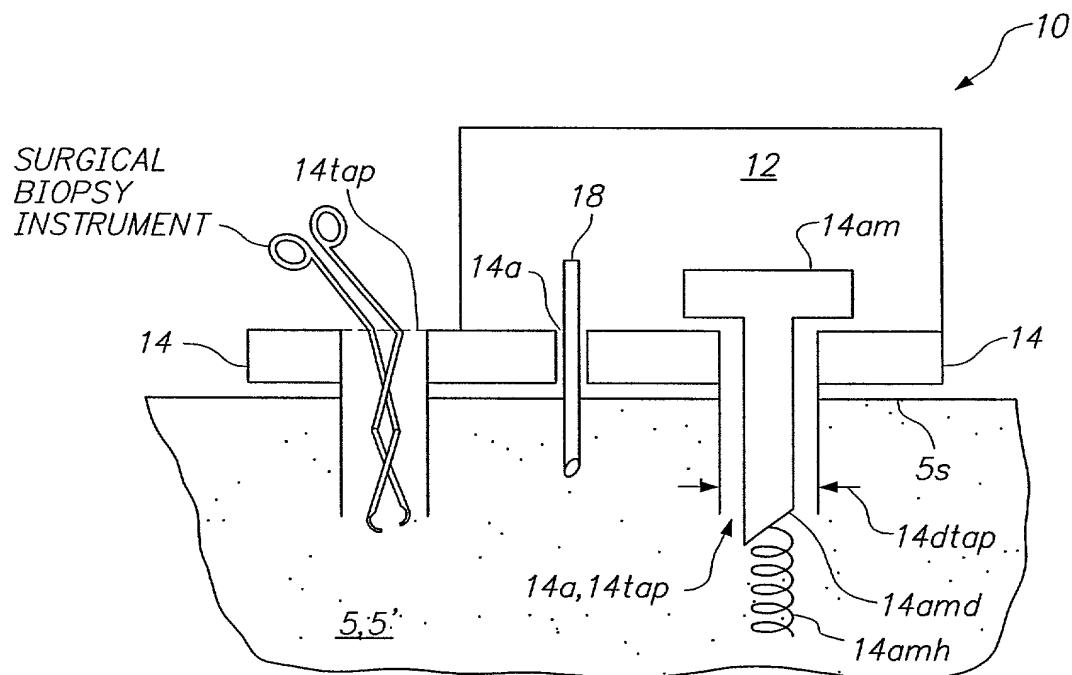


FIG. 15d

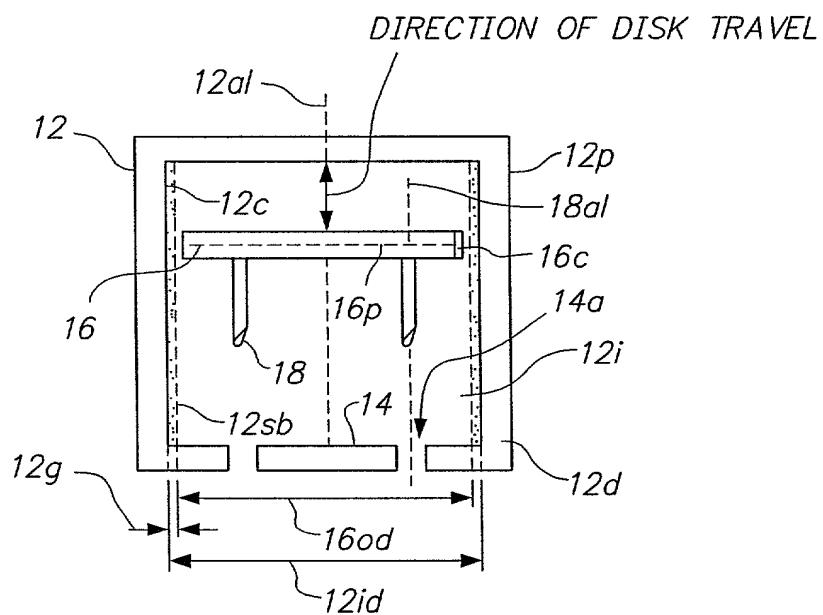


FIG. 16

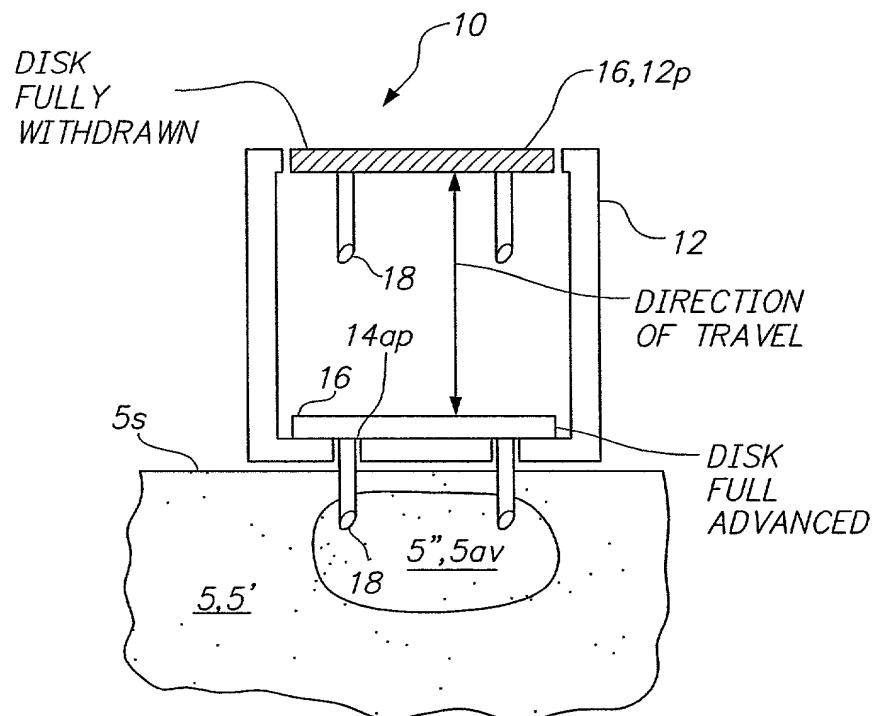


FIG. 17

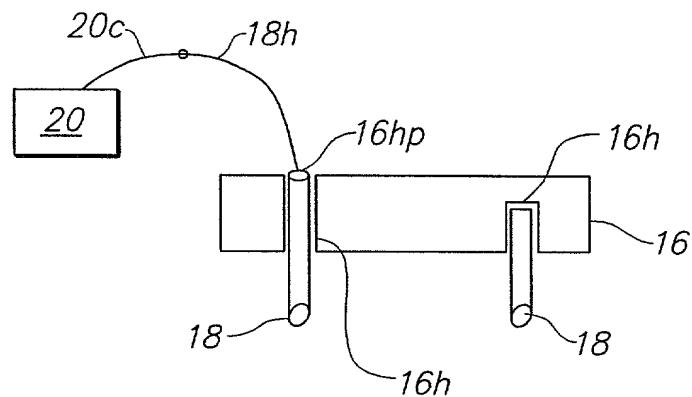
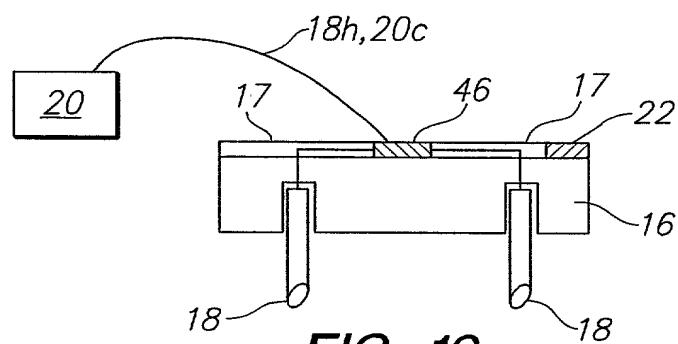
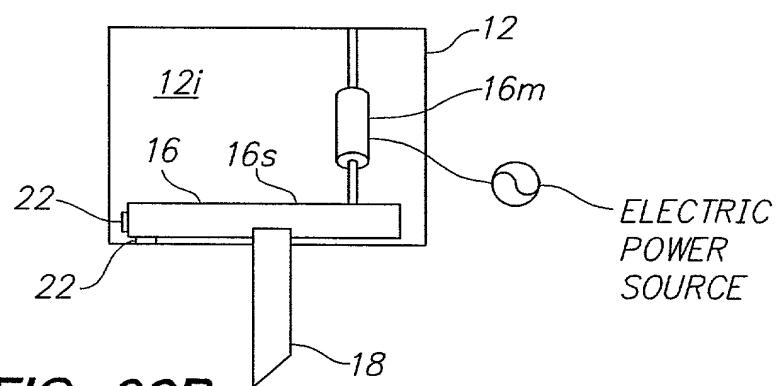
**FIG. 18****FIG. 19****FIG. 20B**

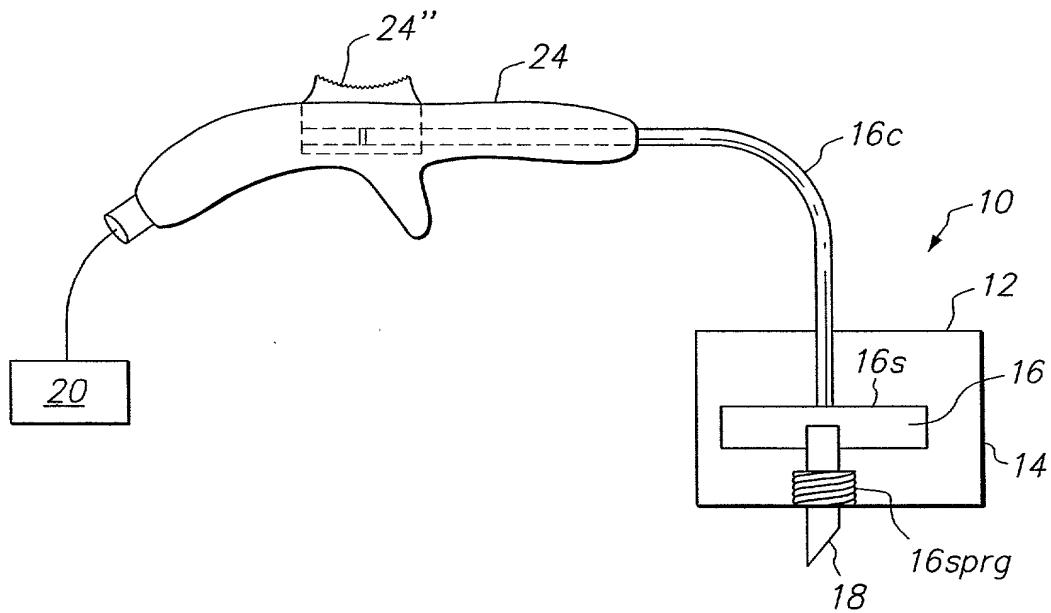
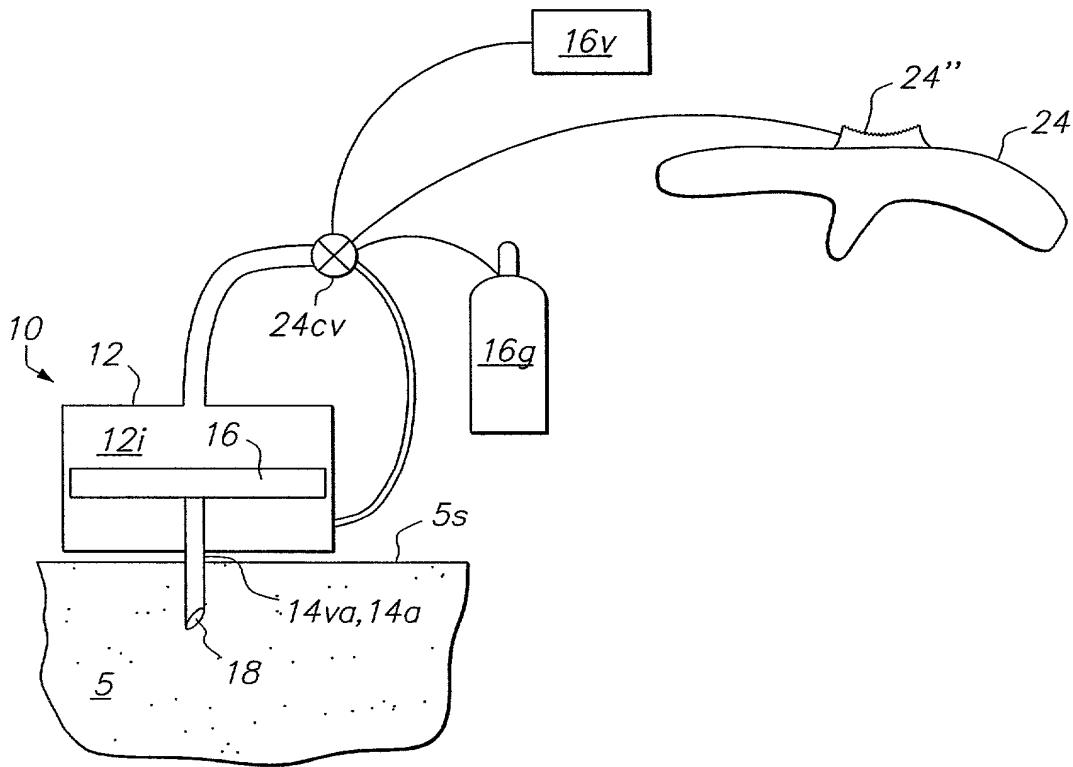
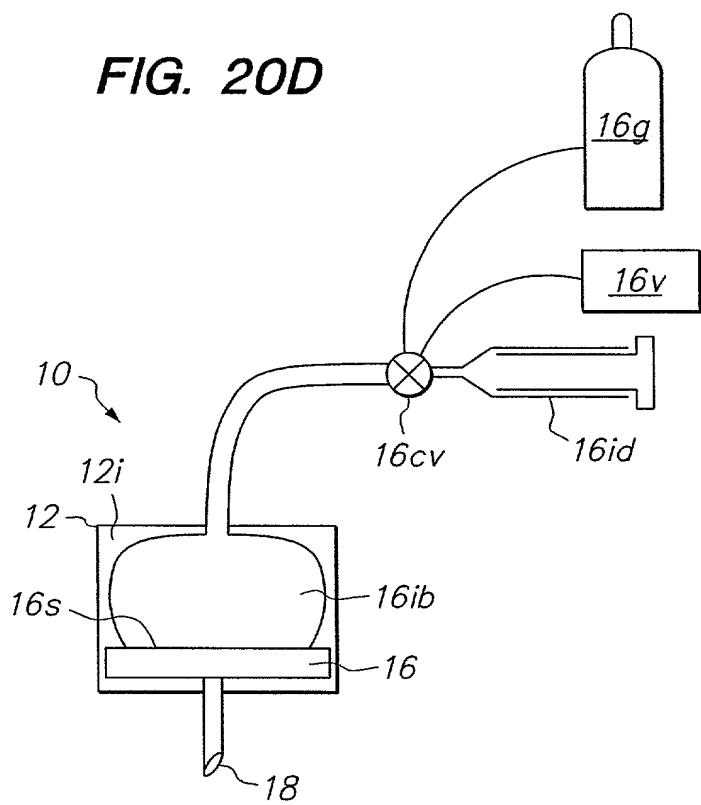
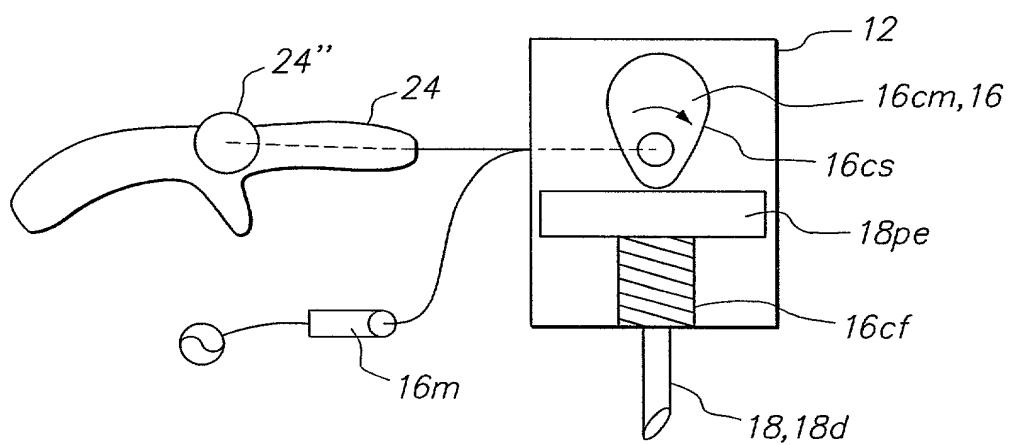
FIG. 20A**FIG. 20C**

FIG. 20D**FIG. 20E**

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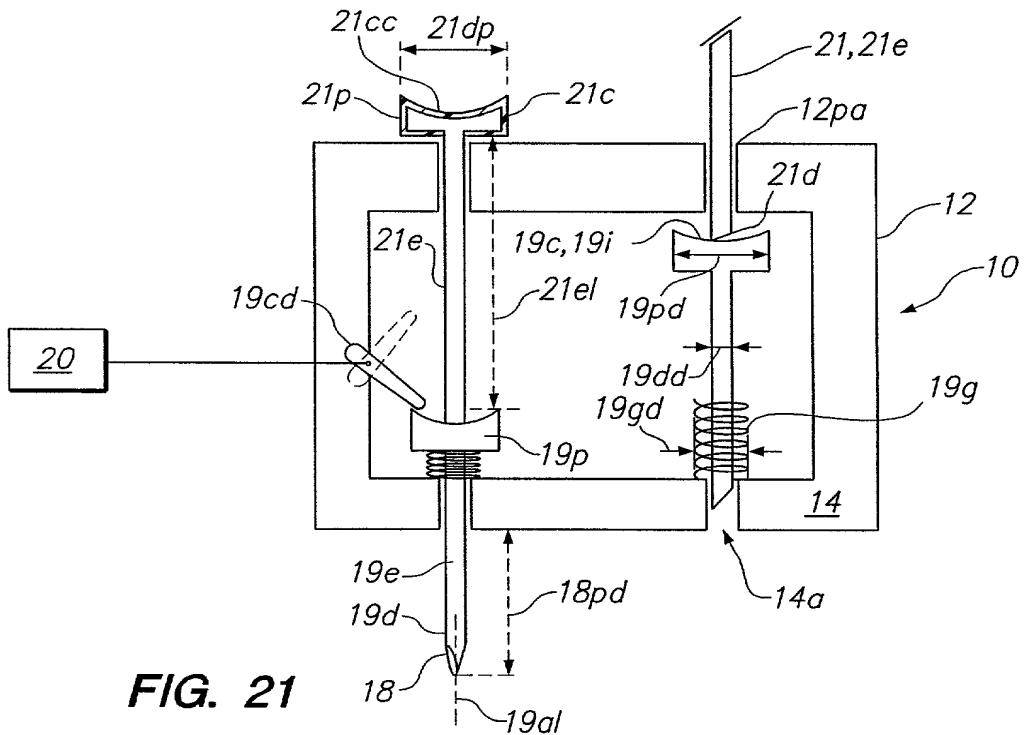


FIG. 21

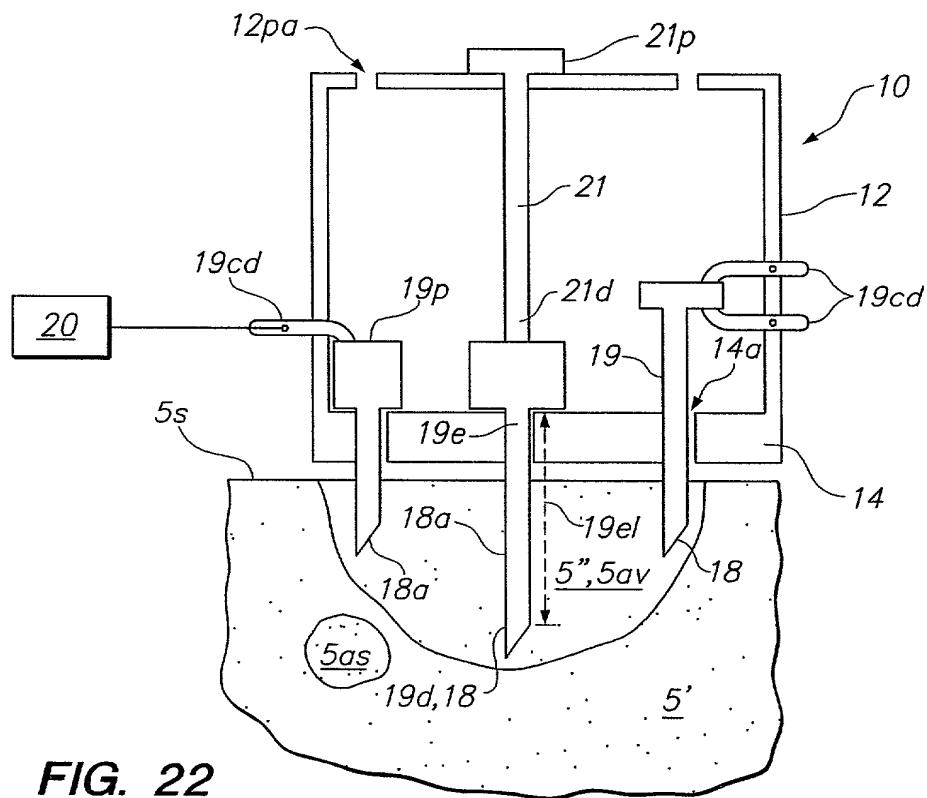


FIG. 22

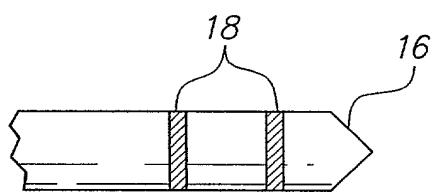


FIG. 23A

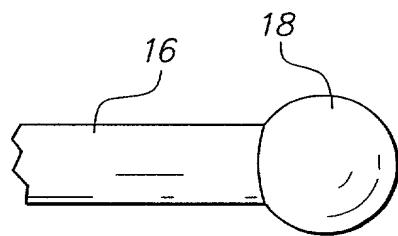


FIG. 23B

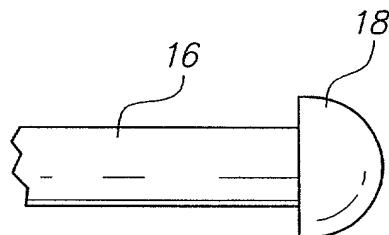


FIG. 23C

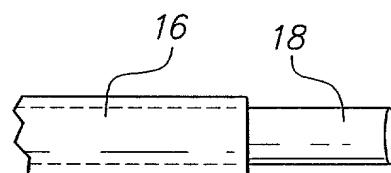


FIG. 23D

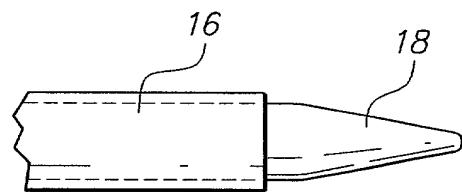


FIG. 23E

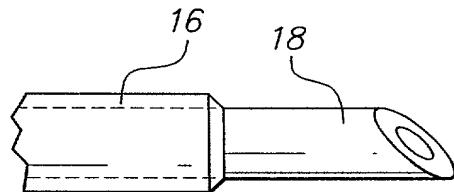


FIG. 23F

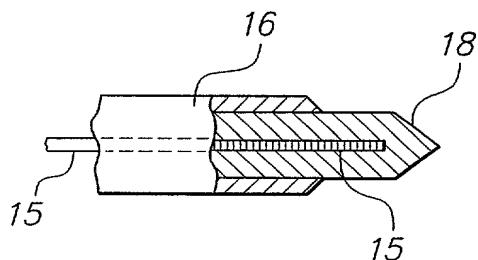


FIG. 23G

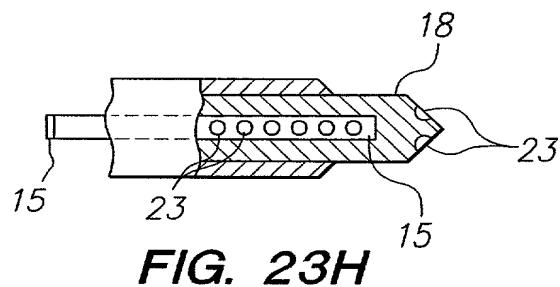
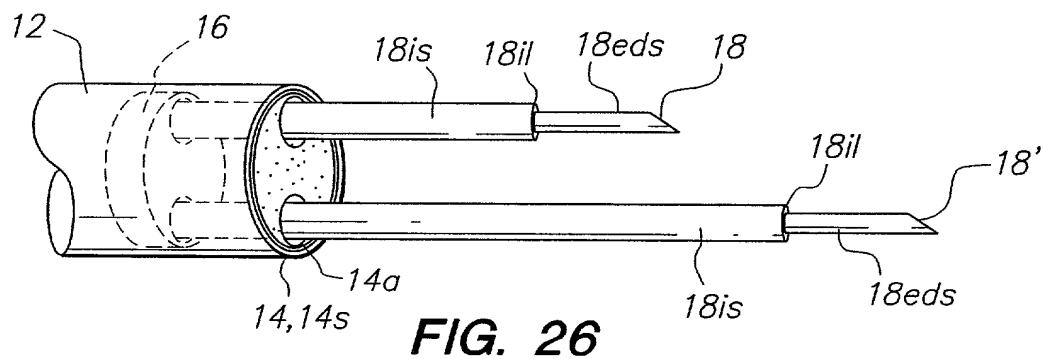
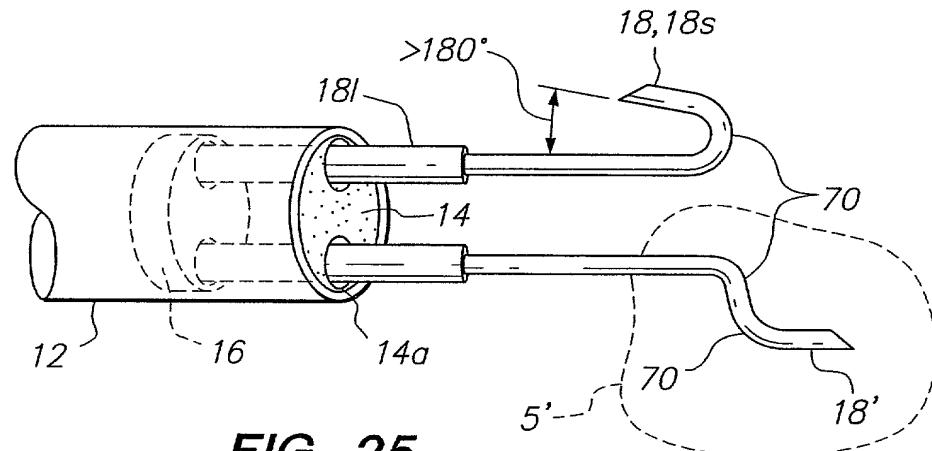
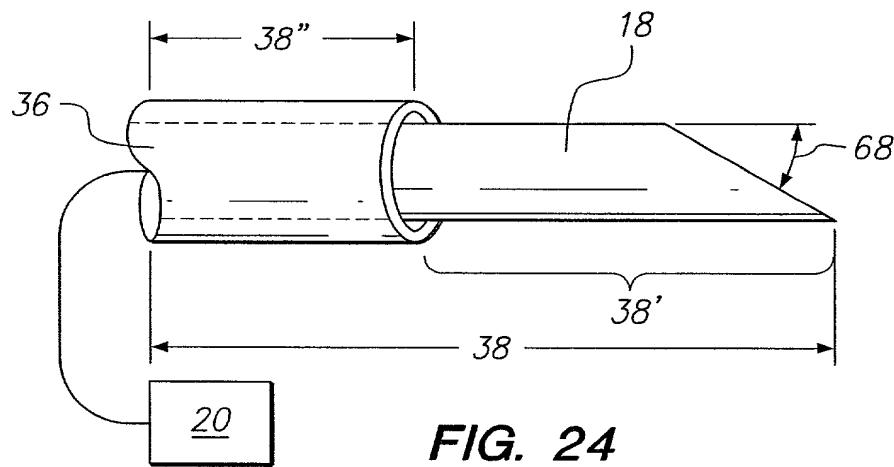


FIG. 23H

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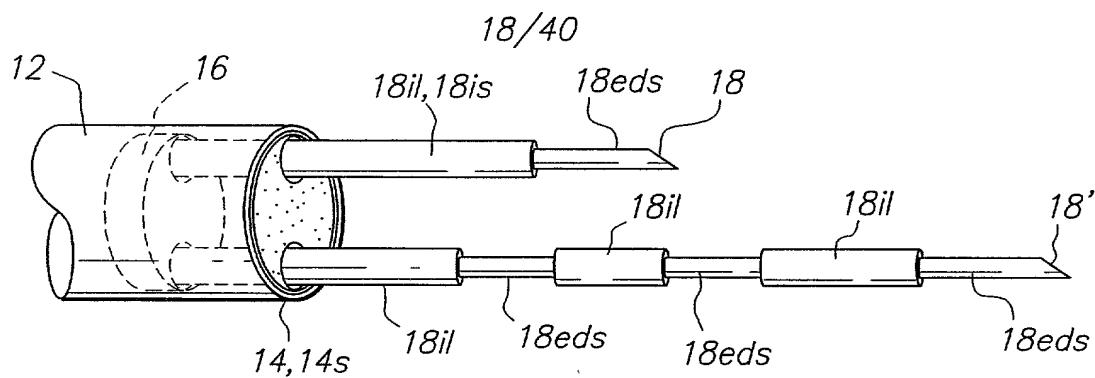


FIG. 27

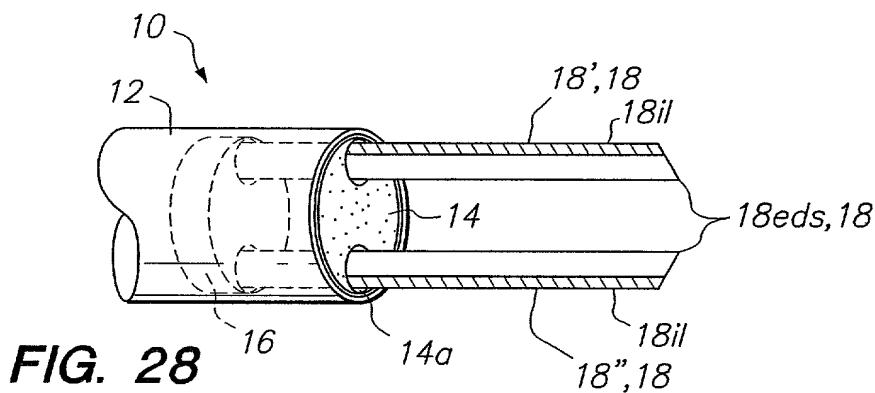


FIG. 28

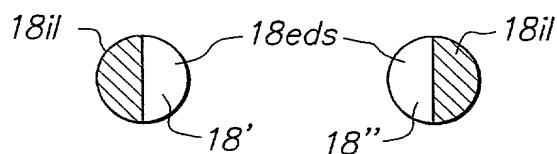


FIG. 29

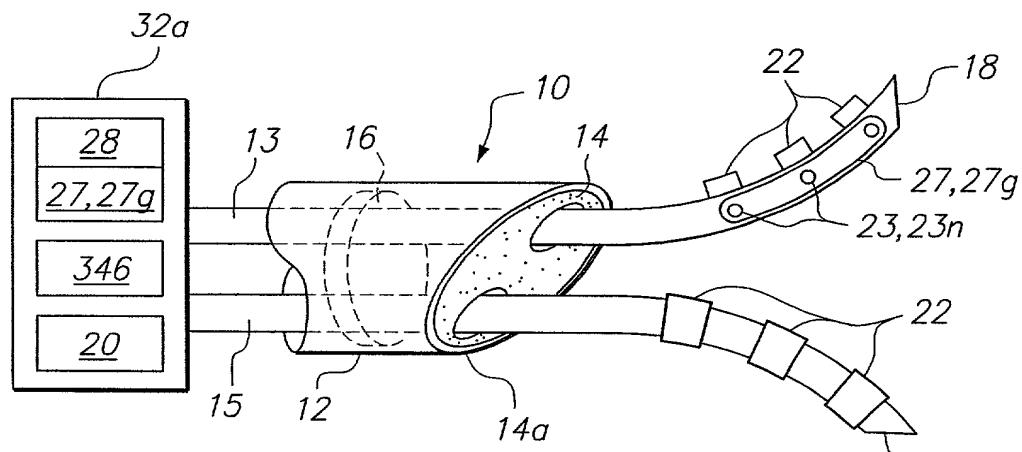


FIG. 30B

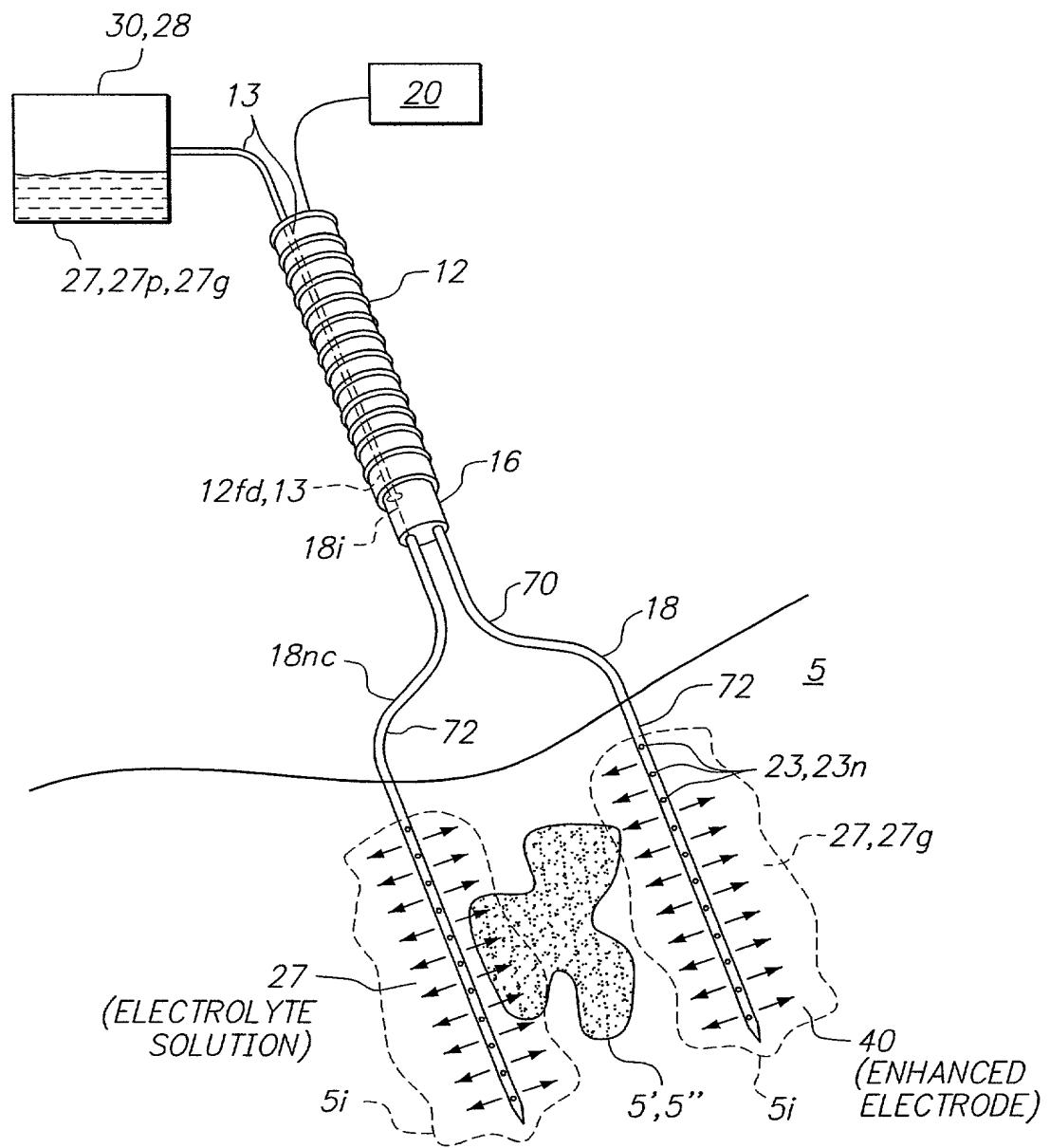


FIG. 30A

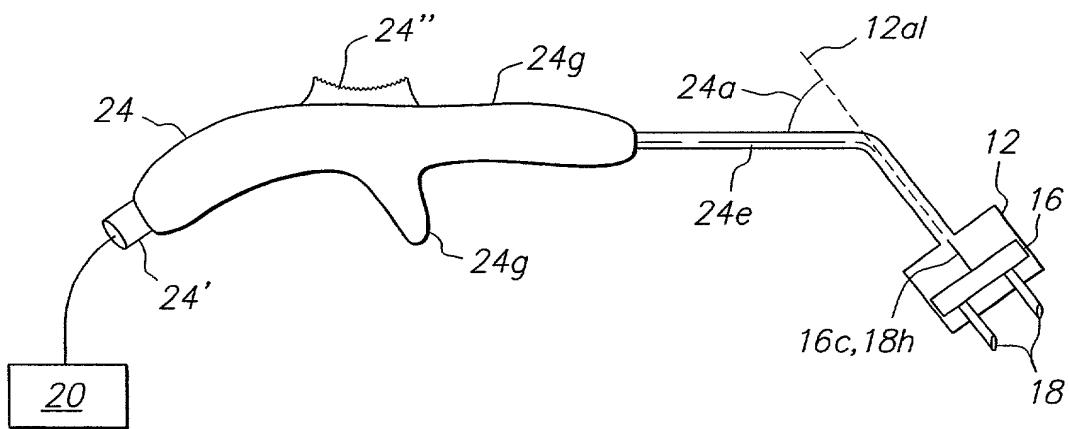
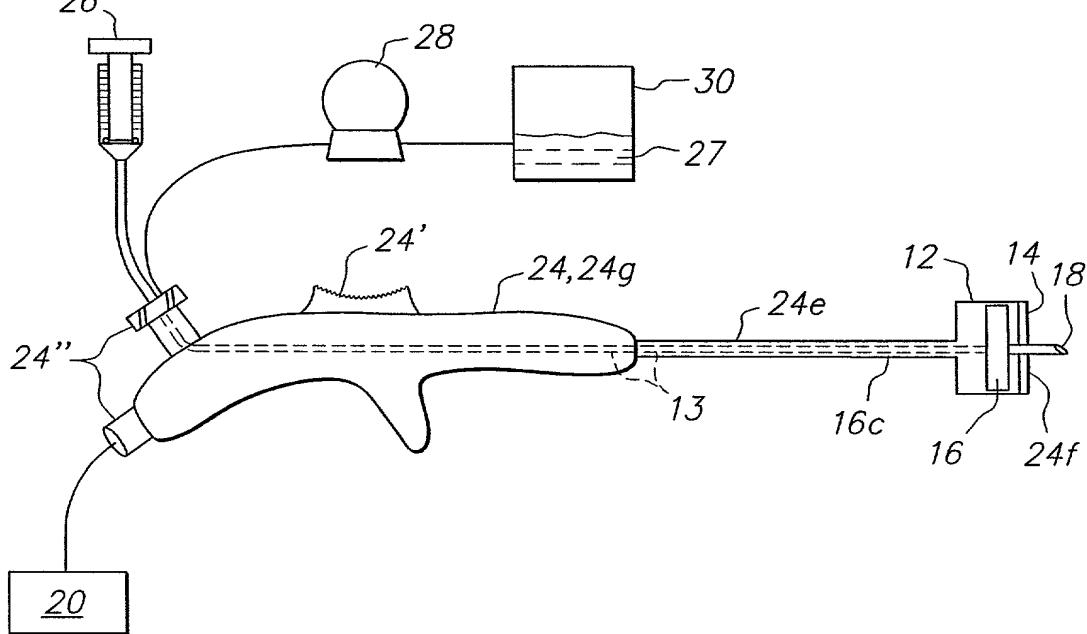
FIG. 31**FIG. 32A**

FIG. 32B

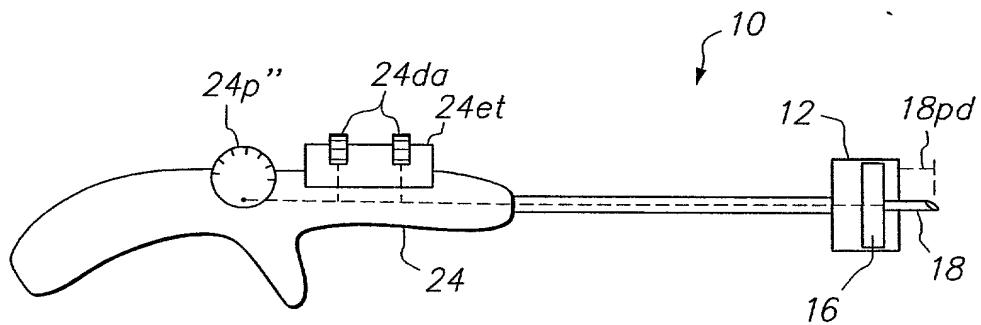


FIG. 33

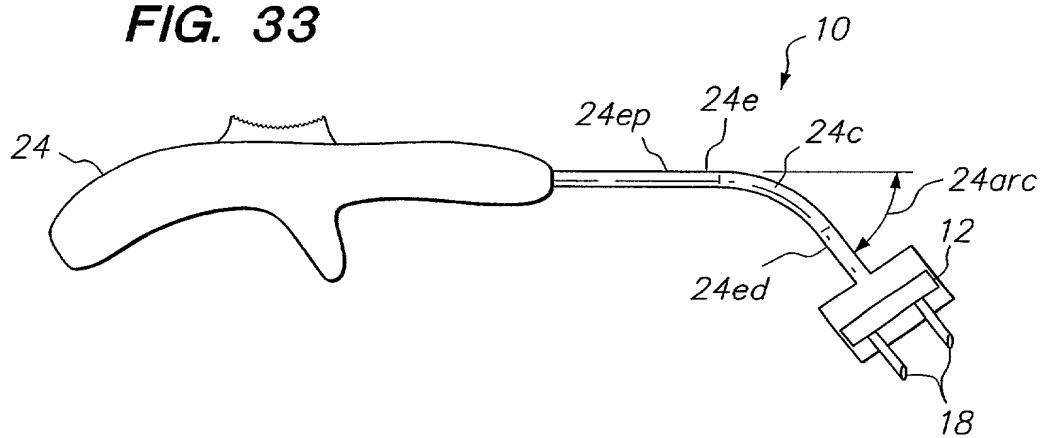


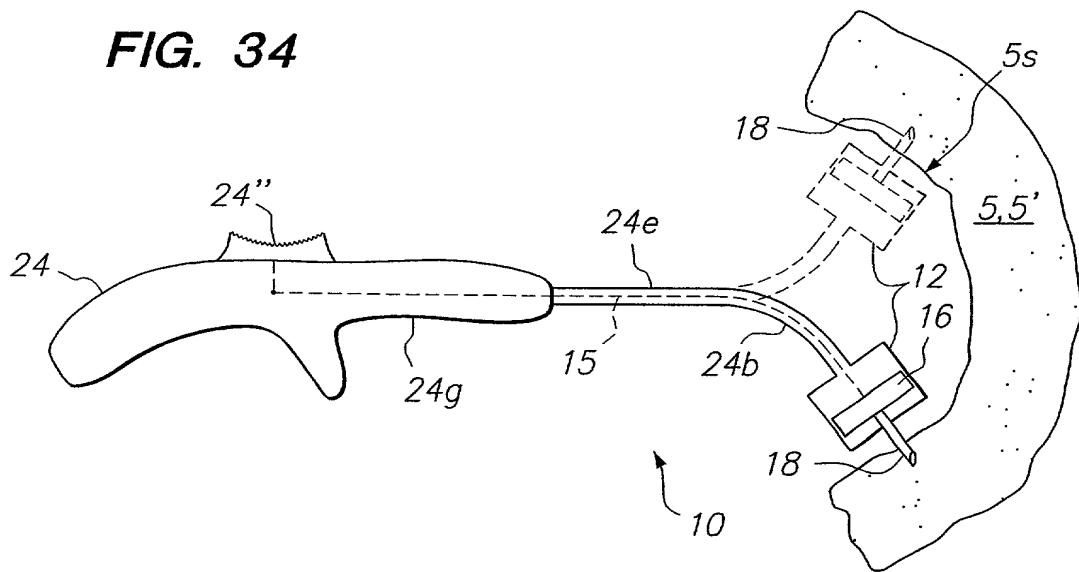
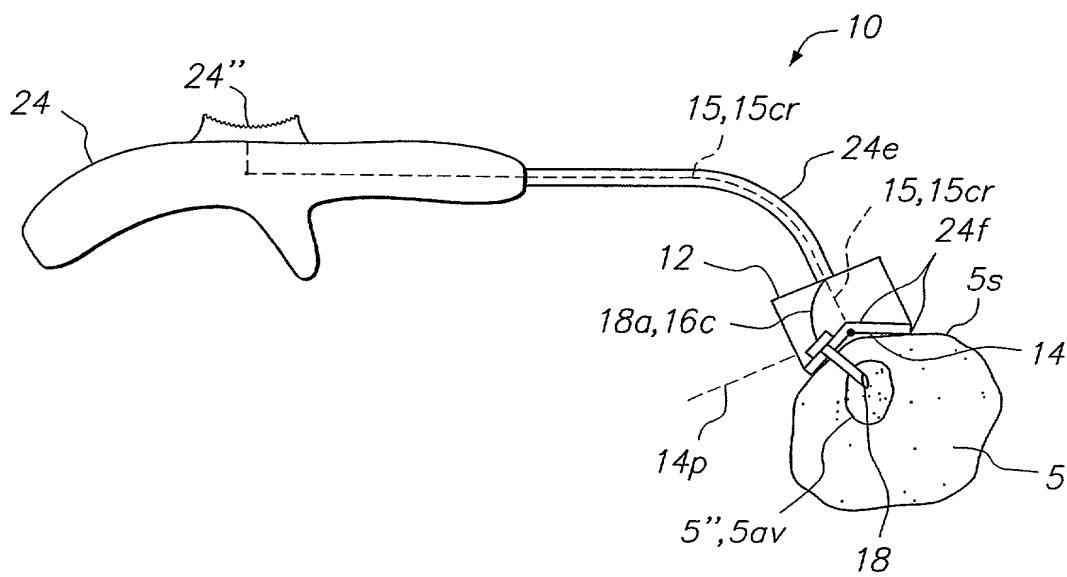
FIG. 34**FIG. 35**

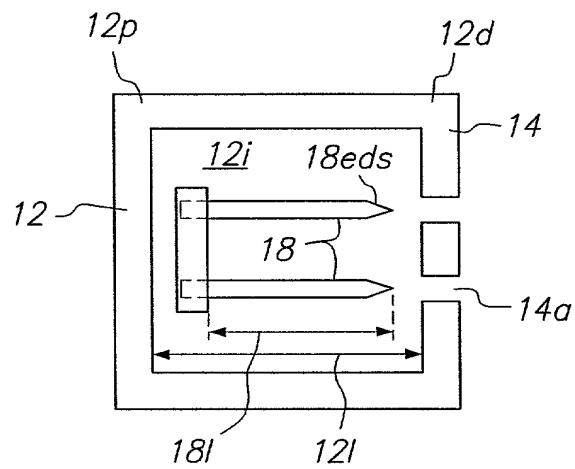
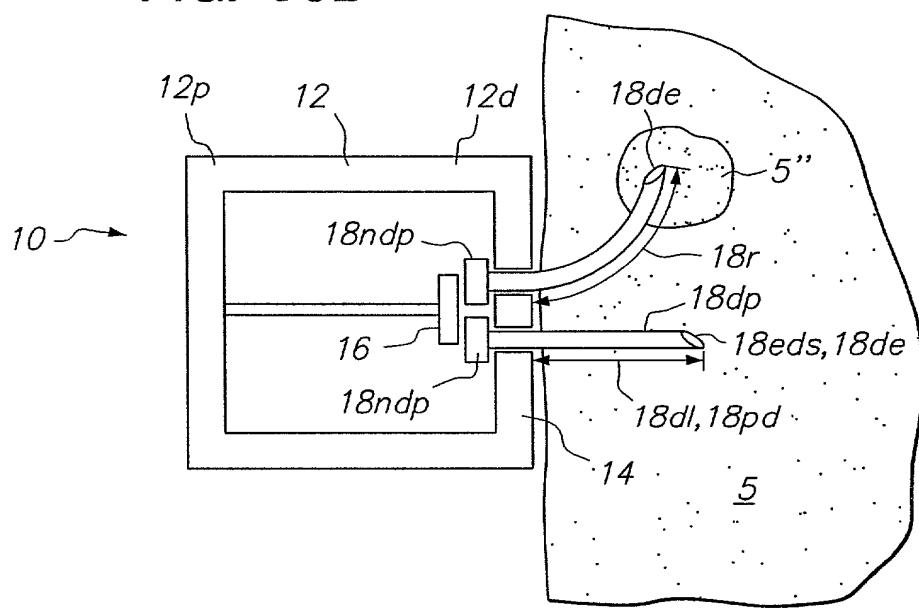
FIG. 36A**FIG. 36B**

FIG. 37

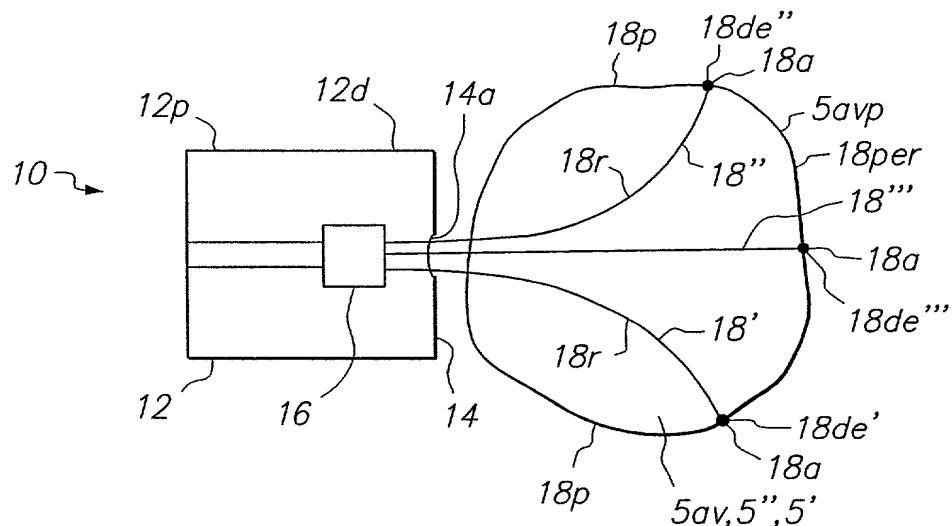


FIG. 38A

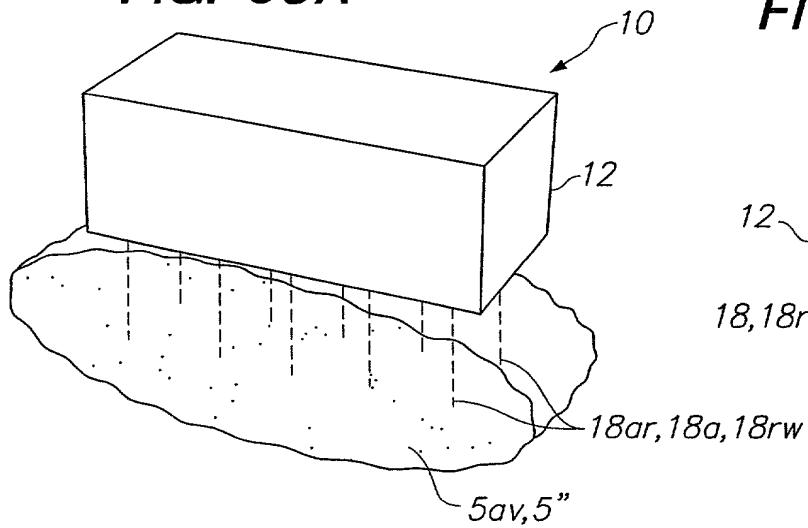


FIG. 38B

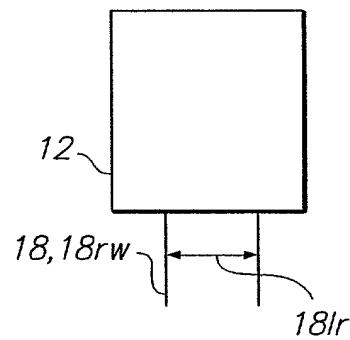
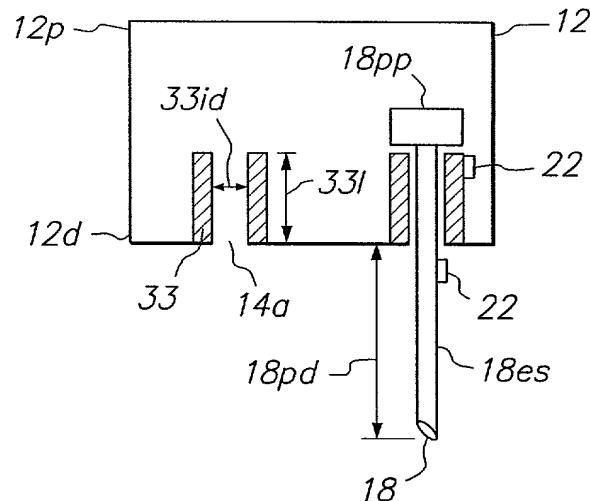
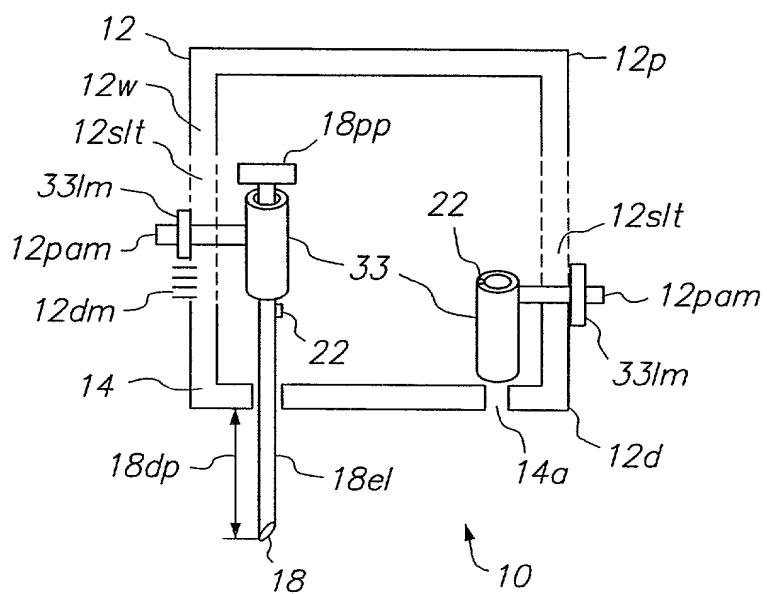
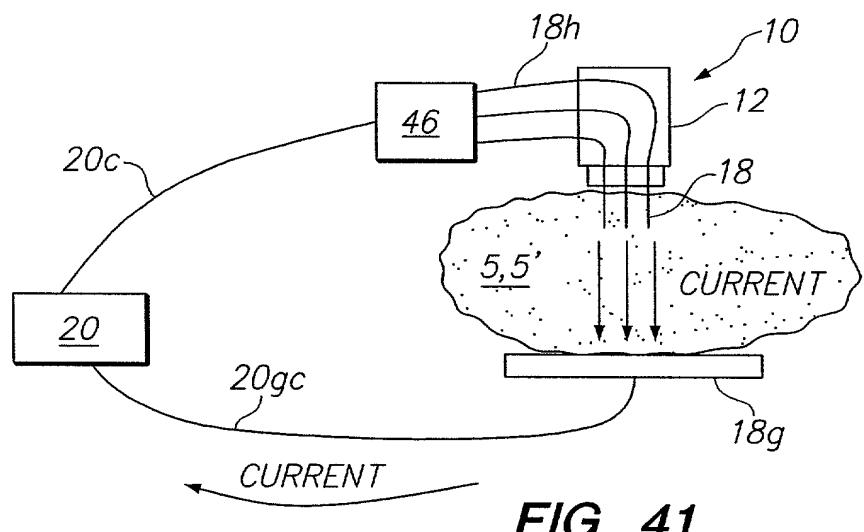


FIG. 39**FIG. 40**



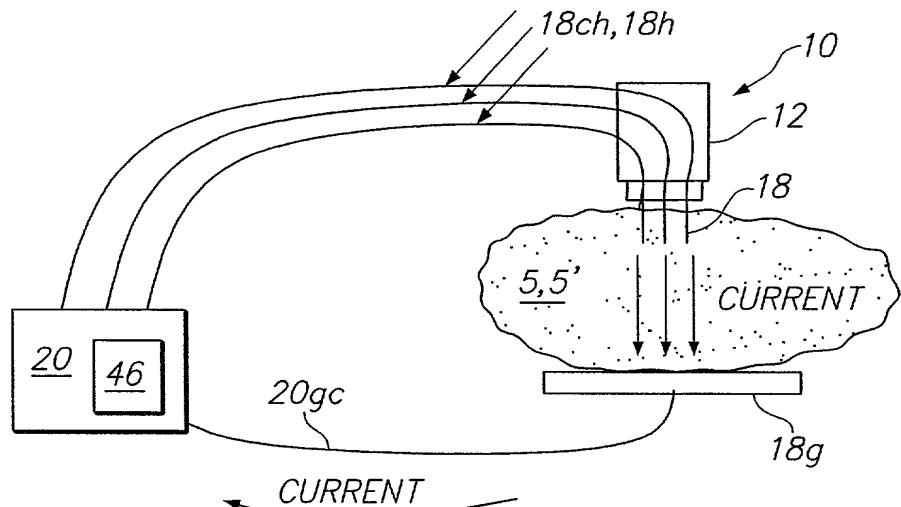
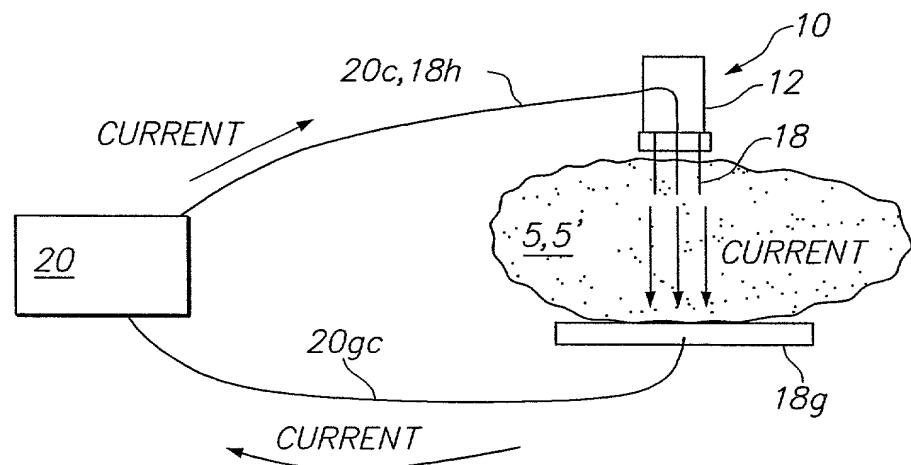
**FIG. 42****FIG. 43**

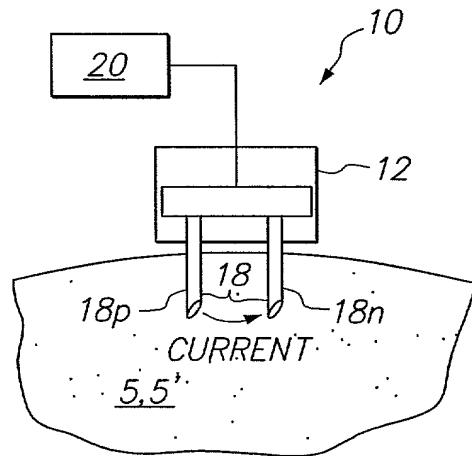
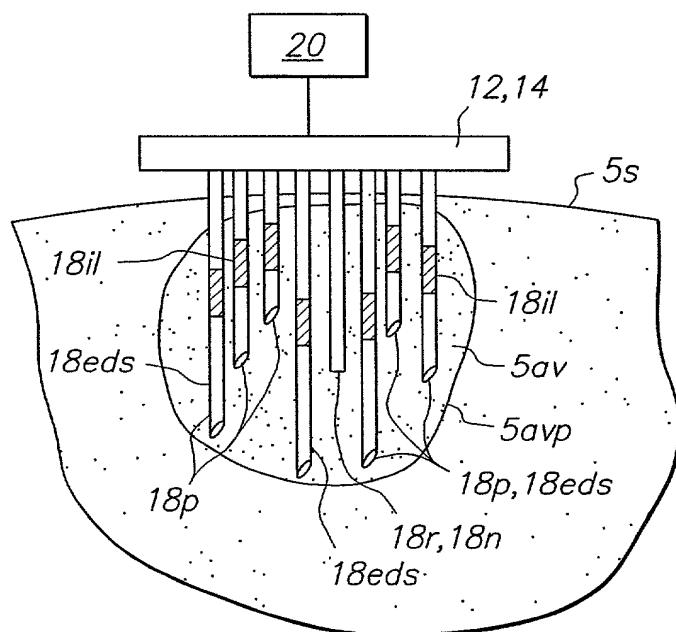
FIG. 44A**FIG. 44B**

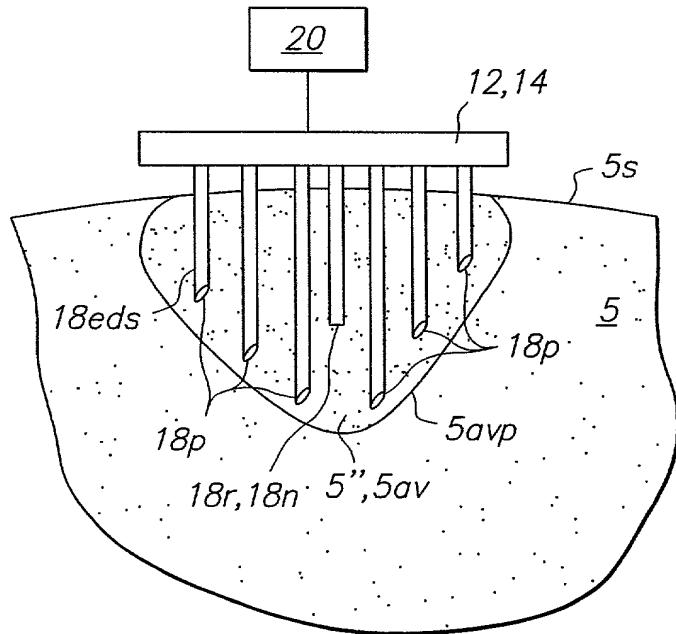
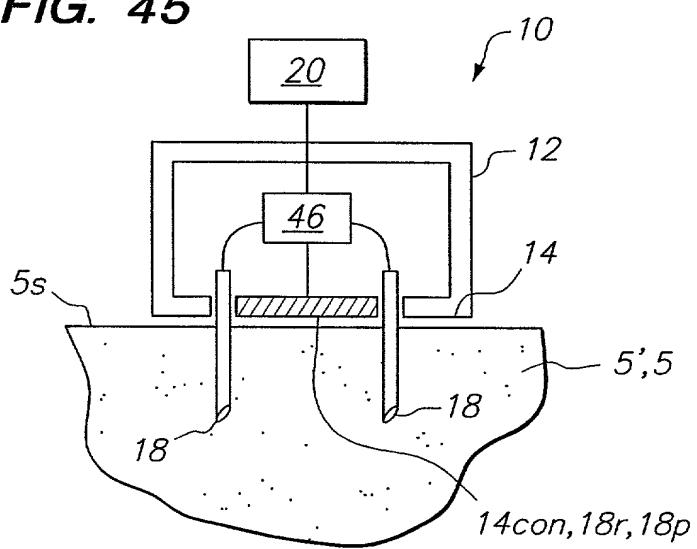
FIG. 44C**FIG. 45**

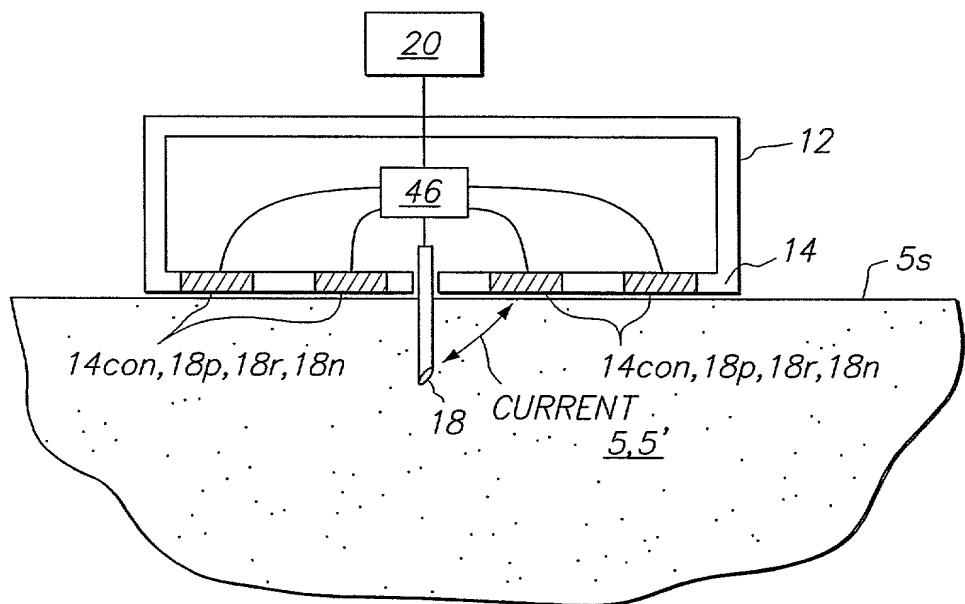
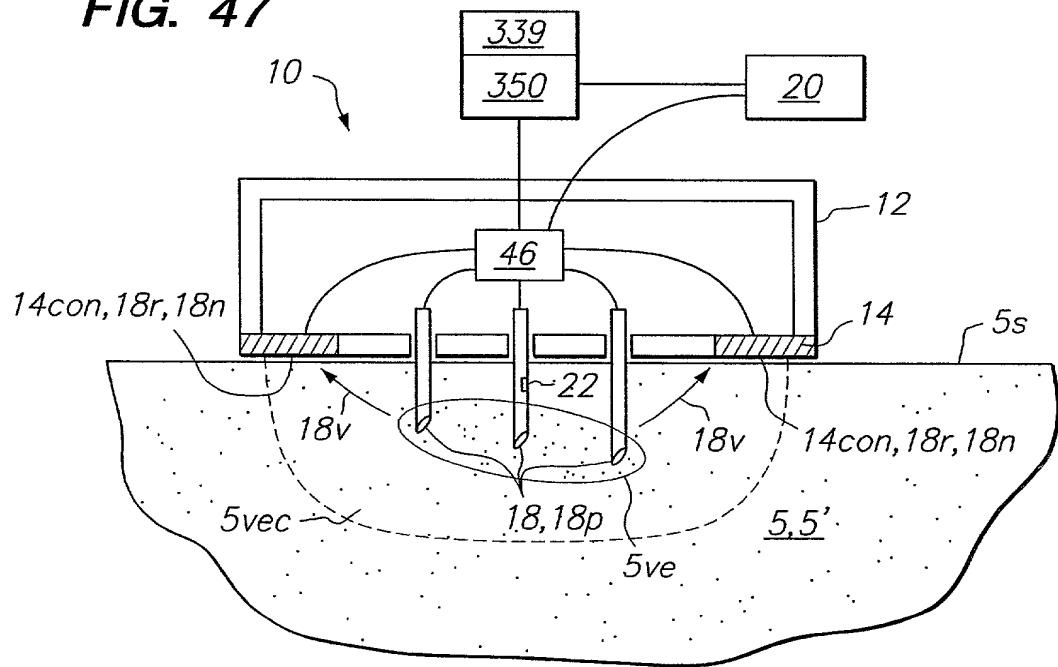
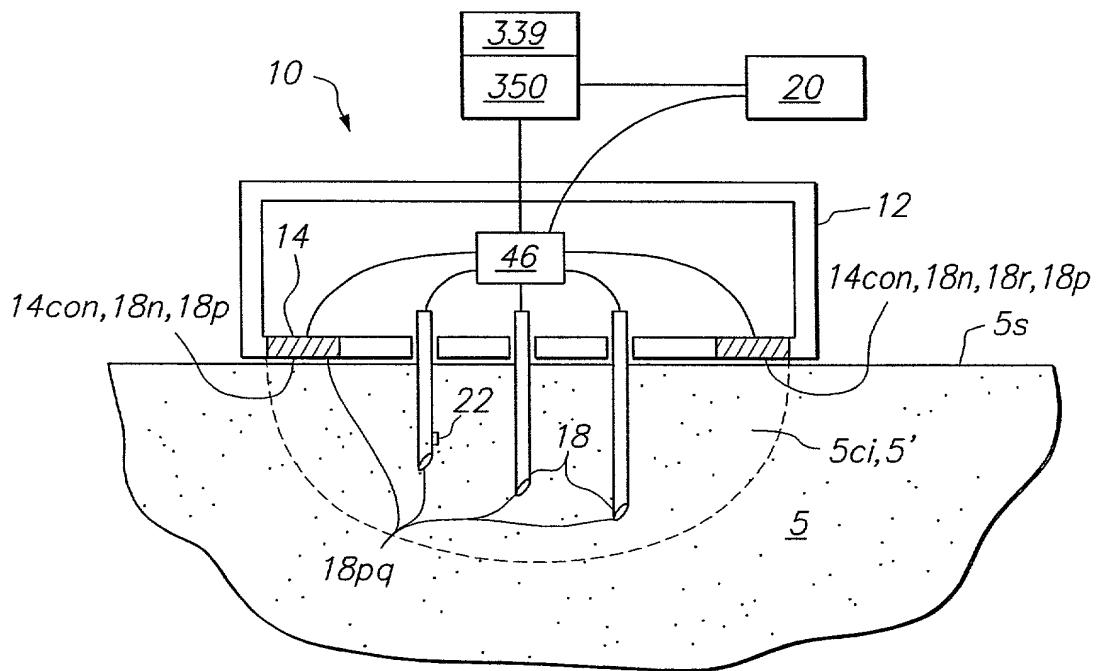
FIG. 46**FIG. 47**

FIG. 48

WIPER FINGER SPACER THERMISTOR IN
SILICON CHIP QUADRATIC INTEGRATOR
AND THREE VOLTAGE DIVIDERS

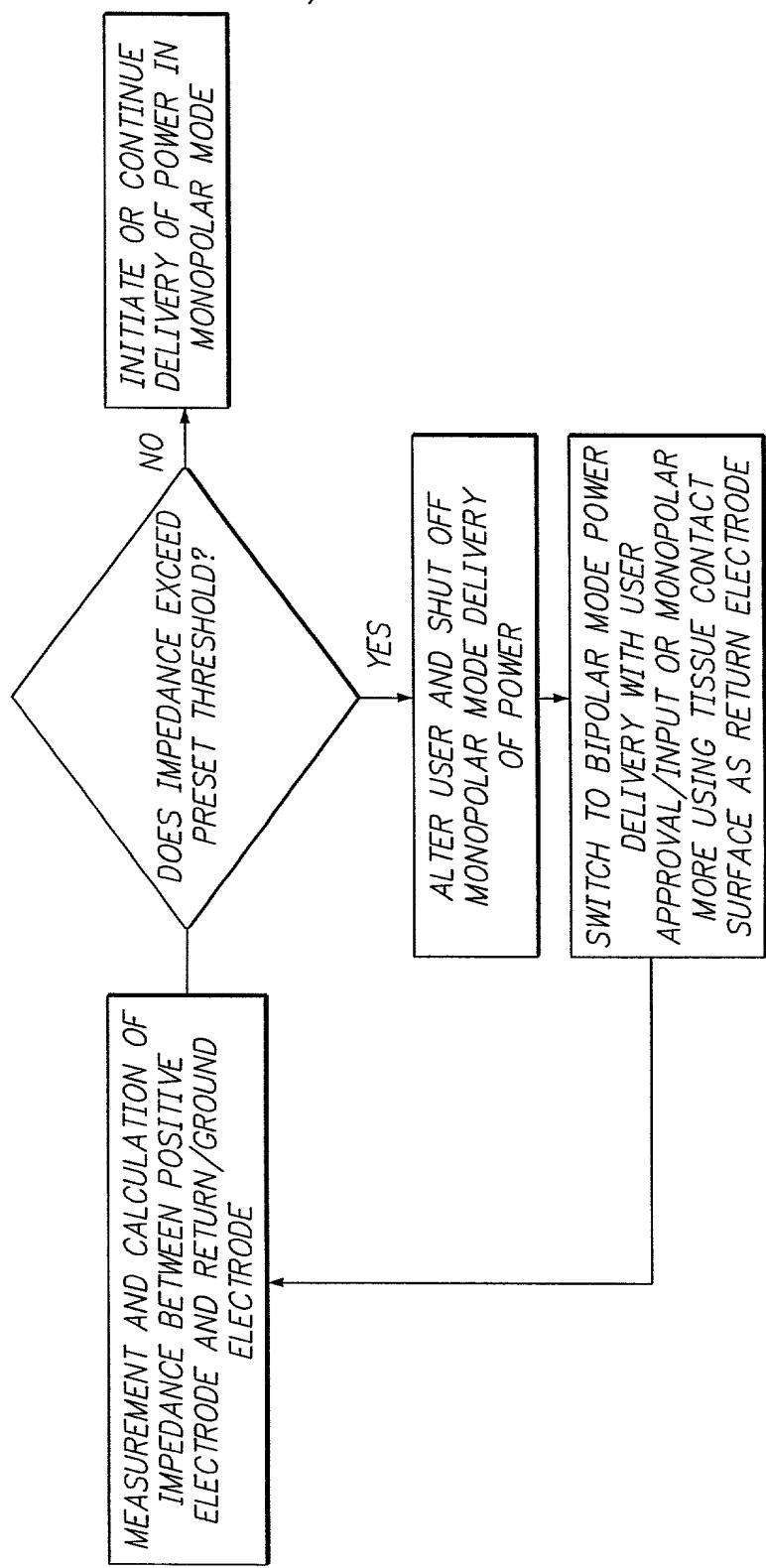


FIG. 49A

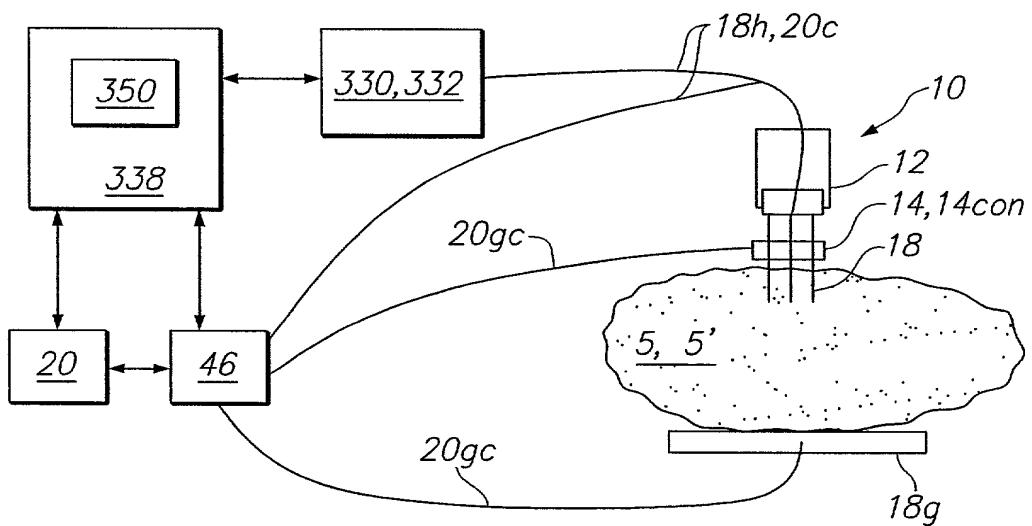


FIG. 49B

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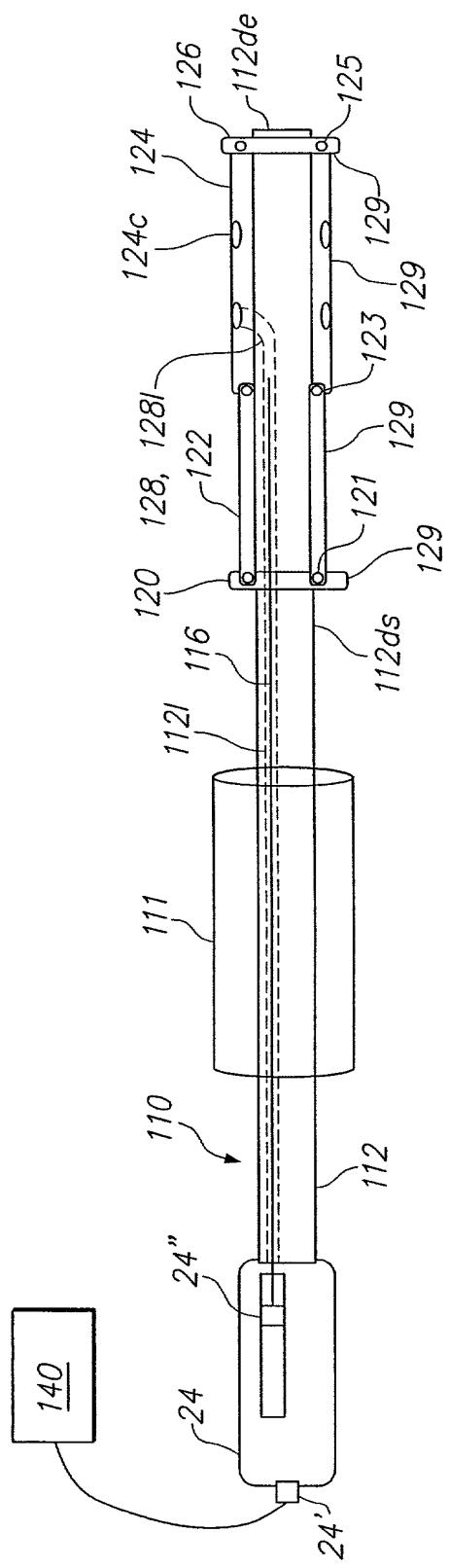


FIG. 50A

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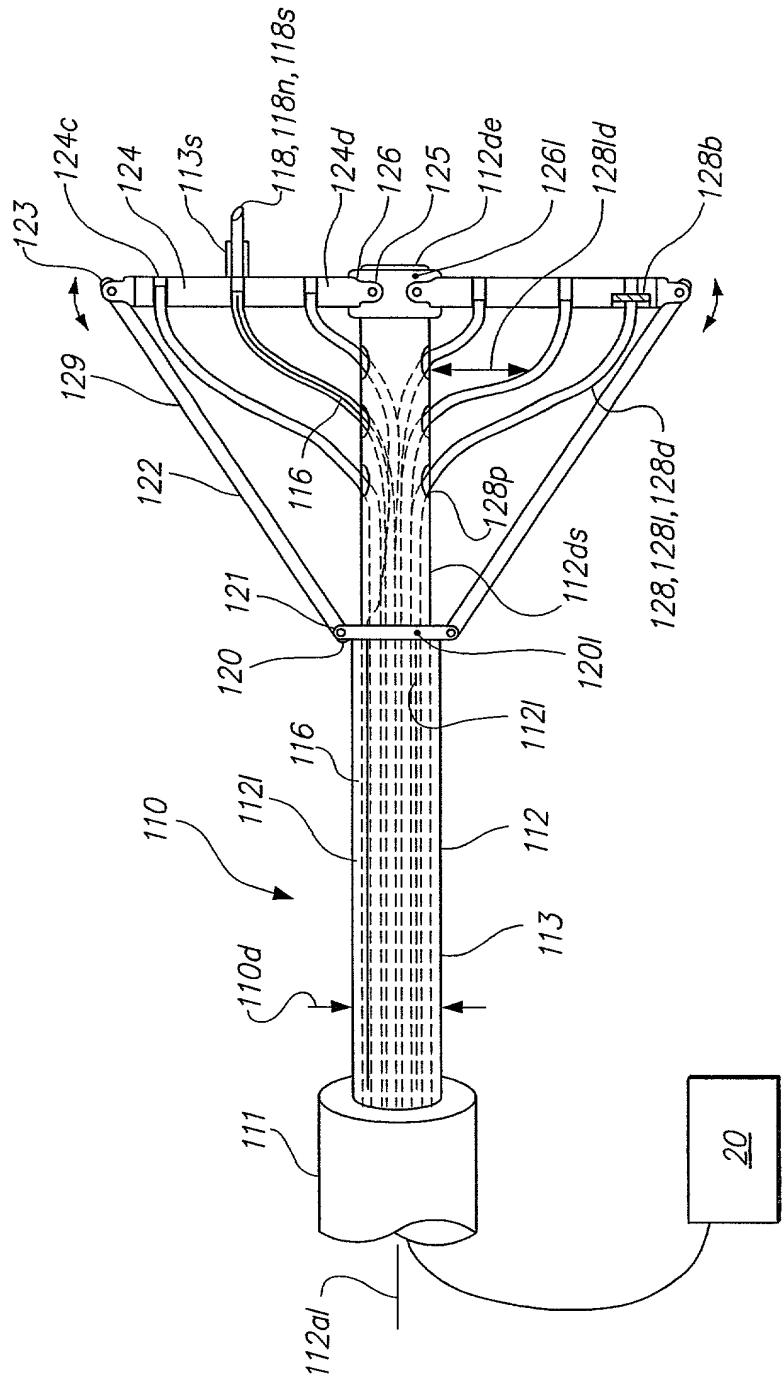
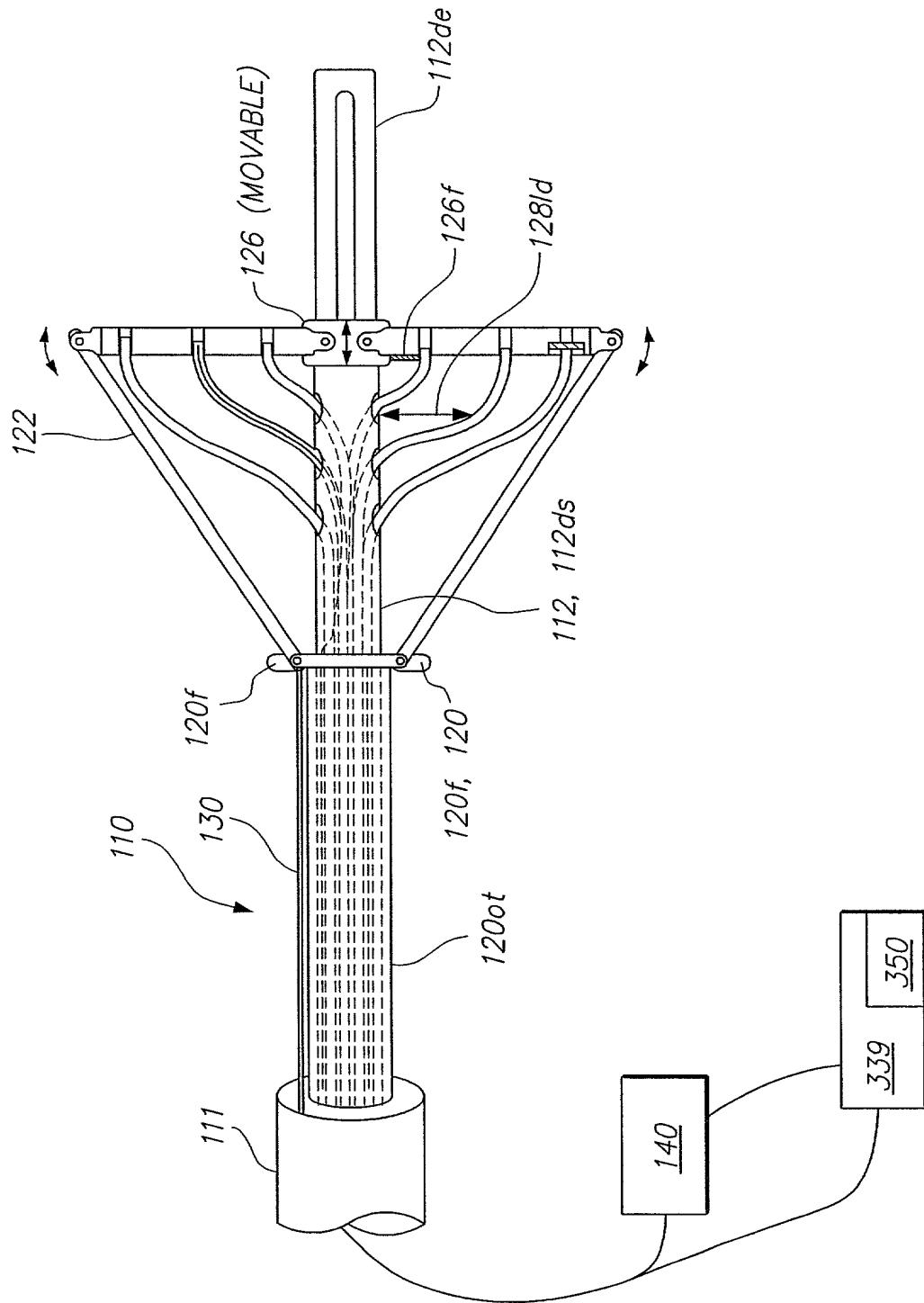


FIG. 50B



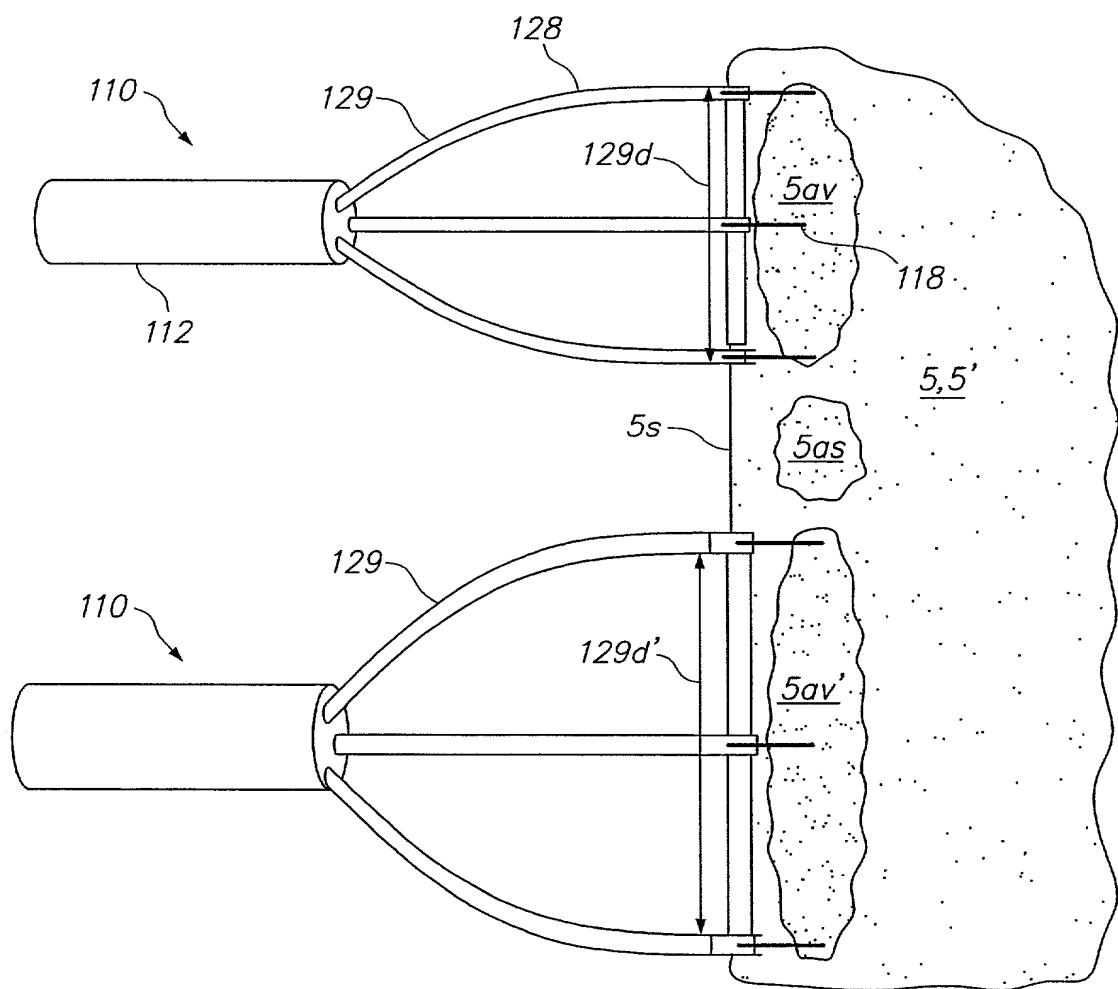
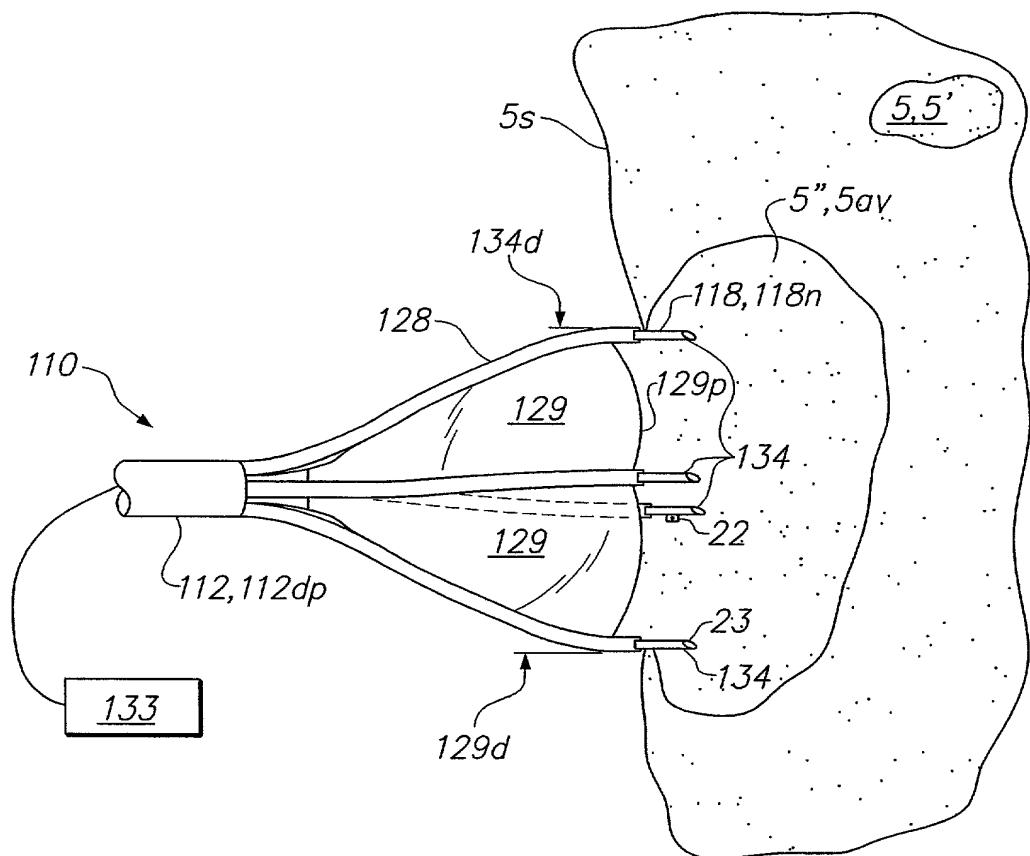
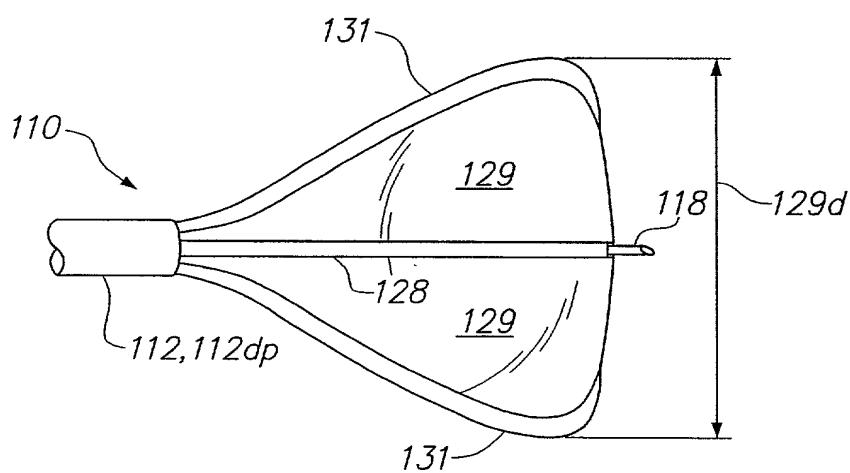


FIG. 52

**FIG. 53A****FIG. 53B**

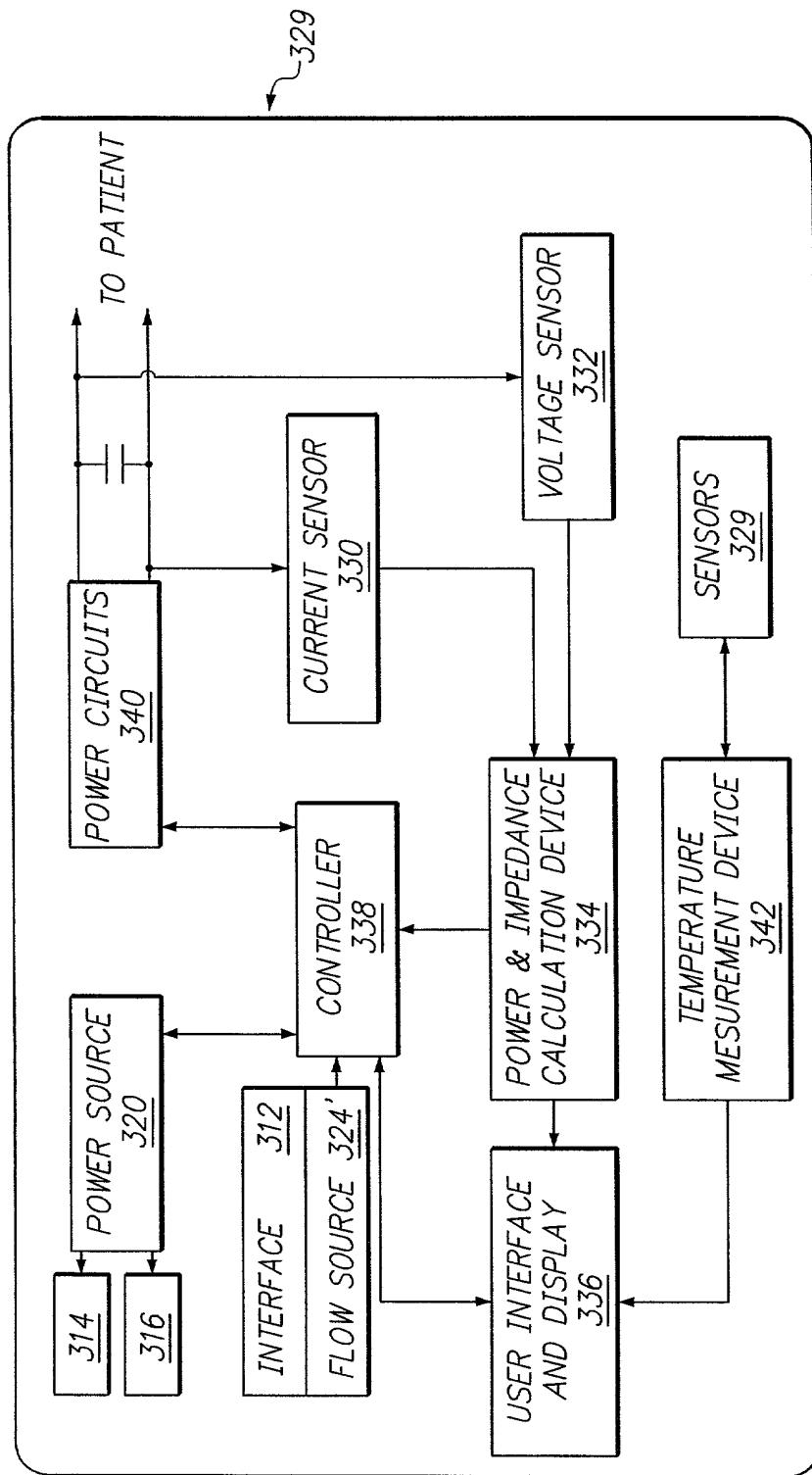


FIG. 54

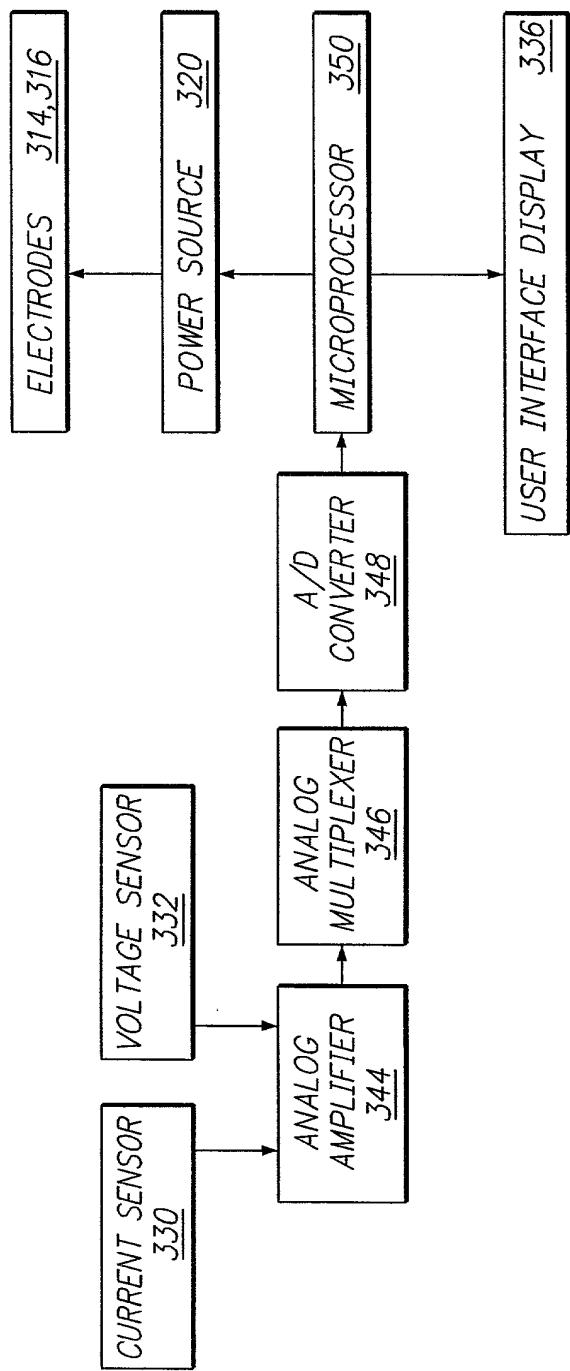


FIG. 55